

# **Synthesis and Verification for All**

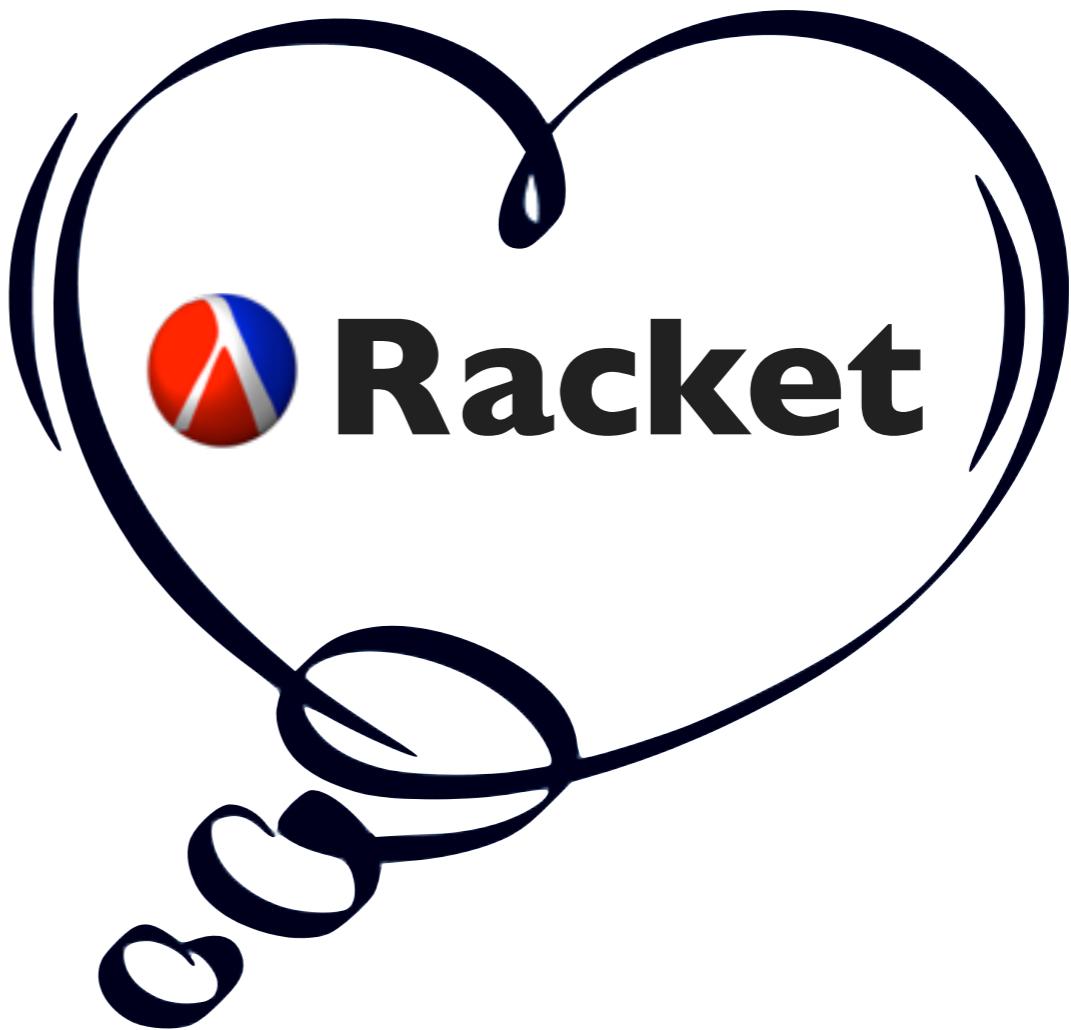
**Emina Torlak**

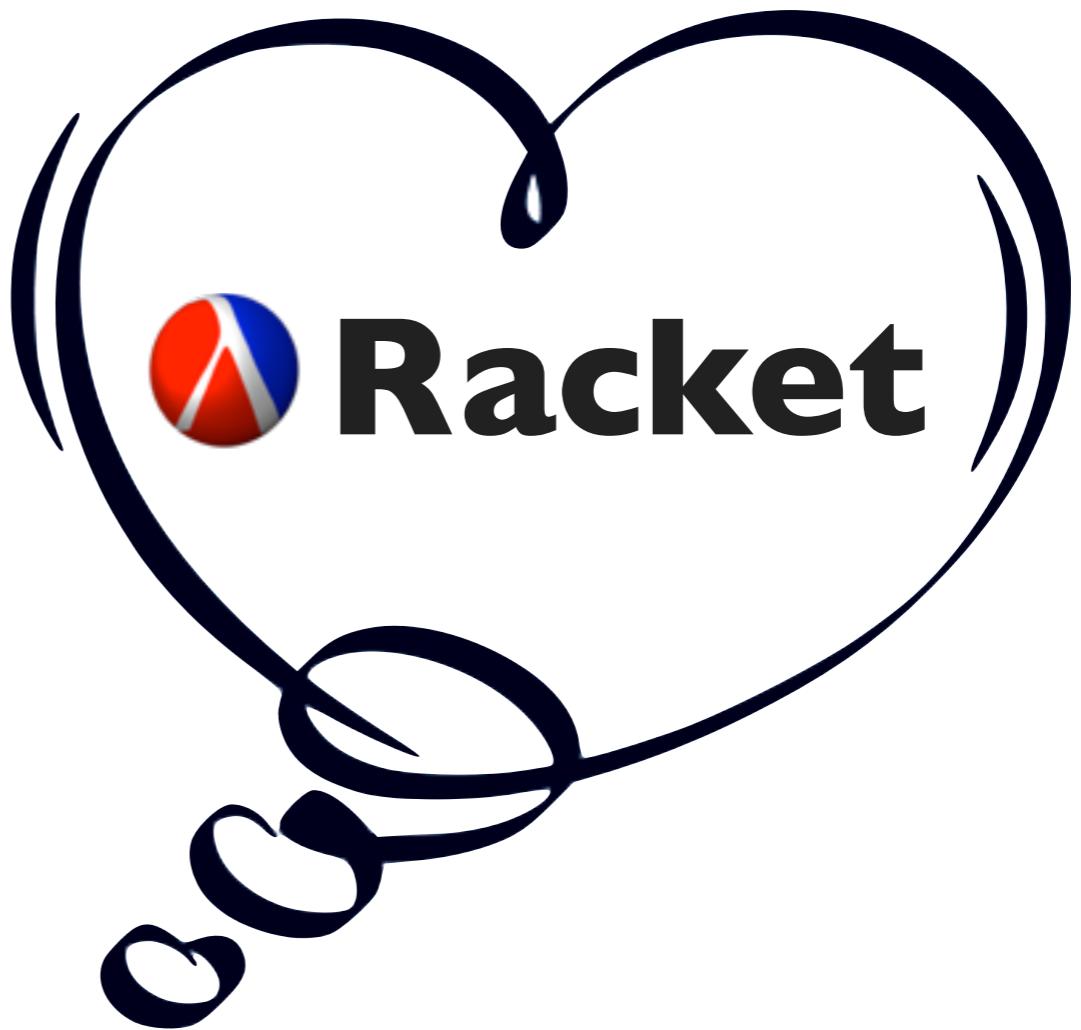
University of Washington

[emina@cs.washington.edu](mailto:emina@cs.washington.edu)

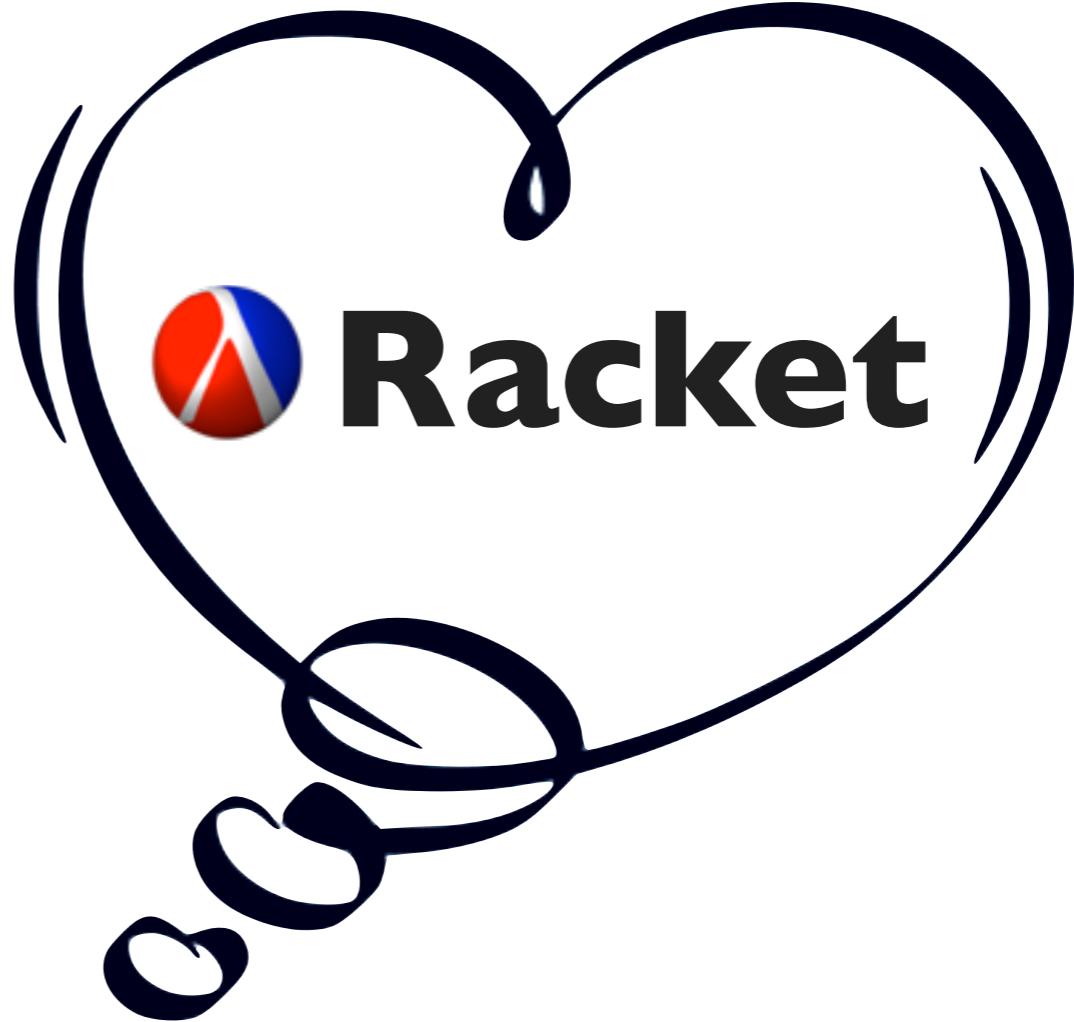
[homes.cs.washington.edu/~emina/](http://homes.cs.washington.edu/~emina/)



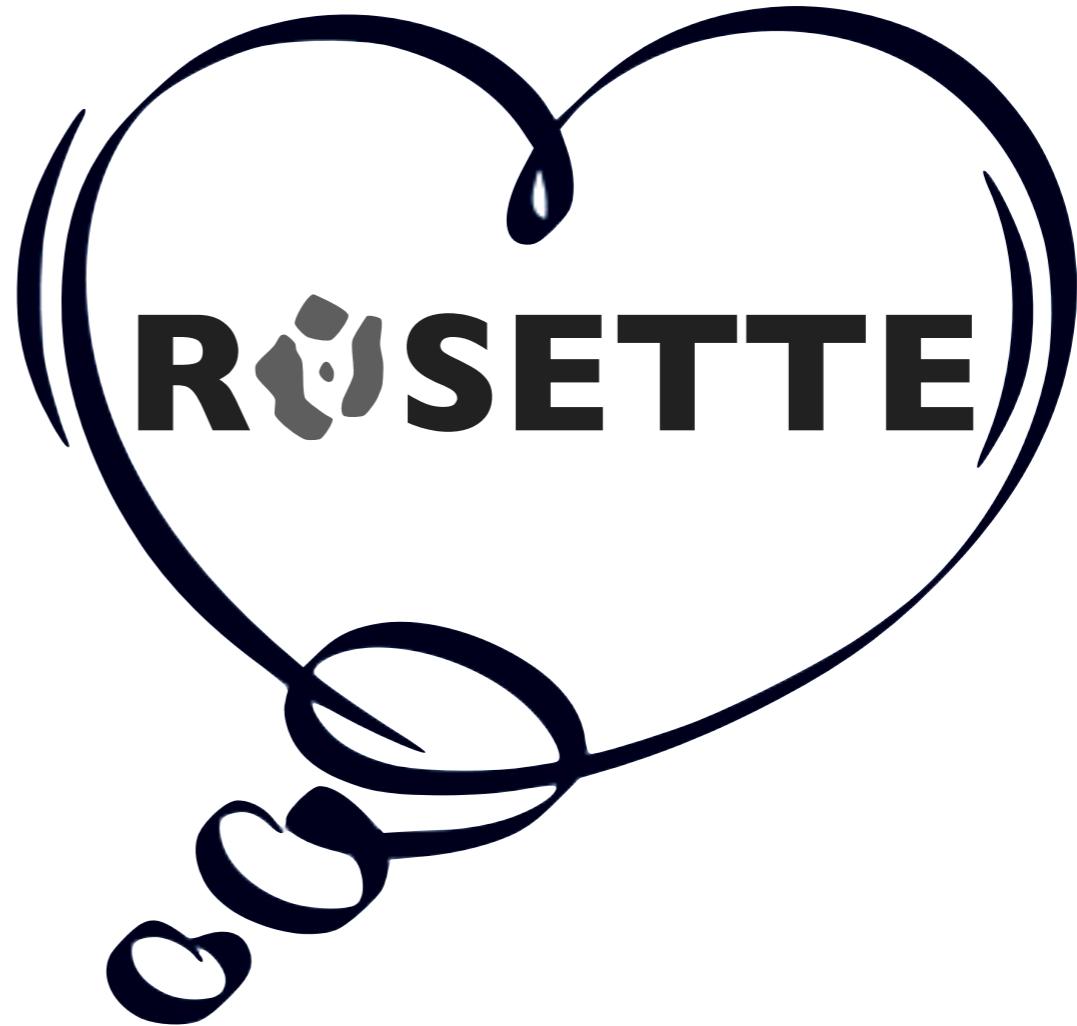
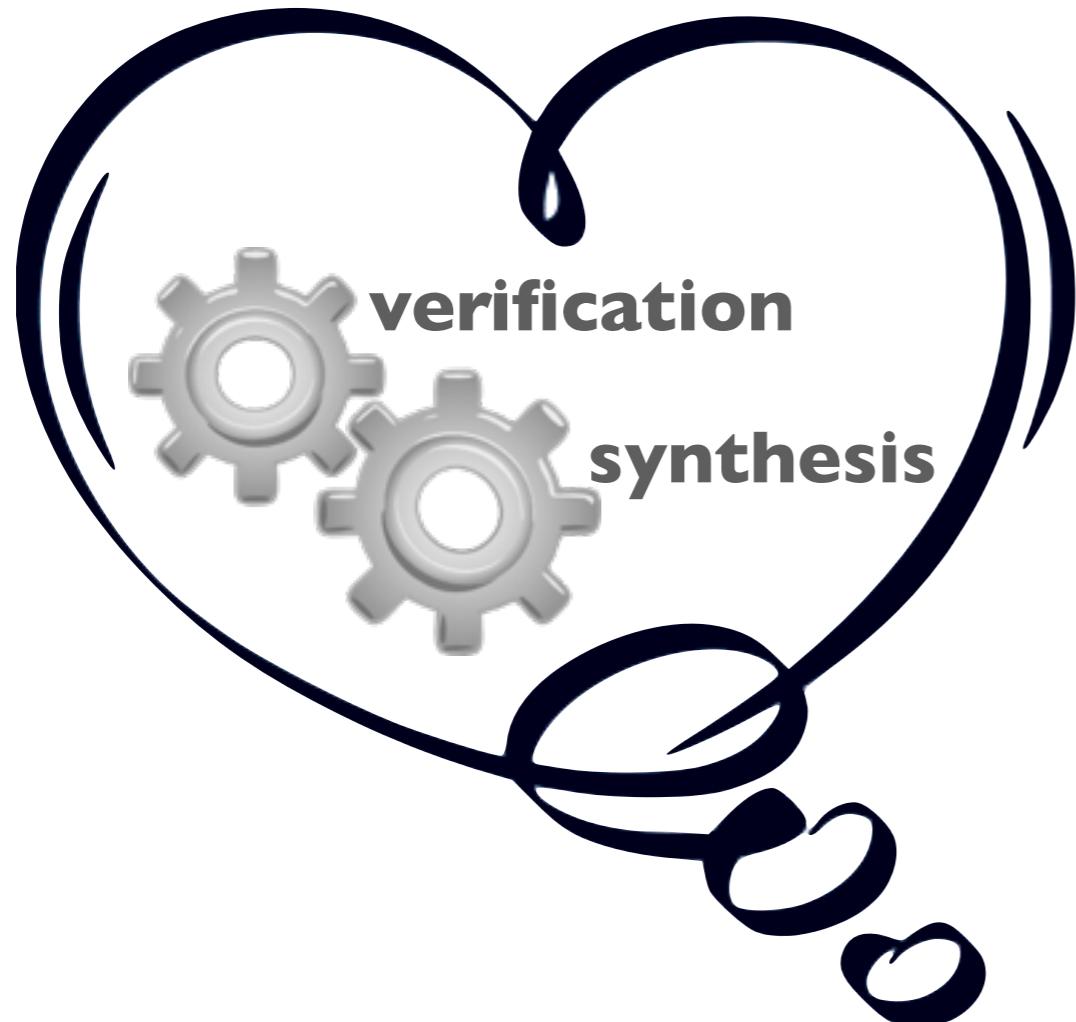




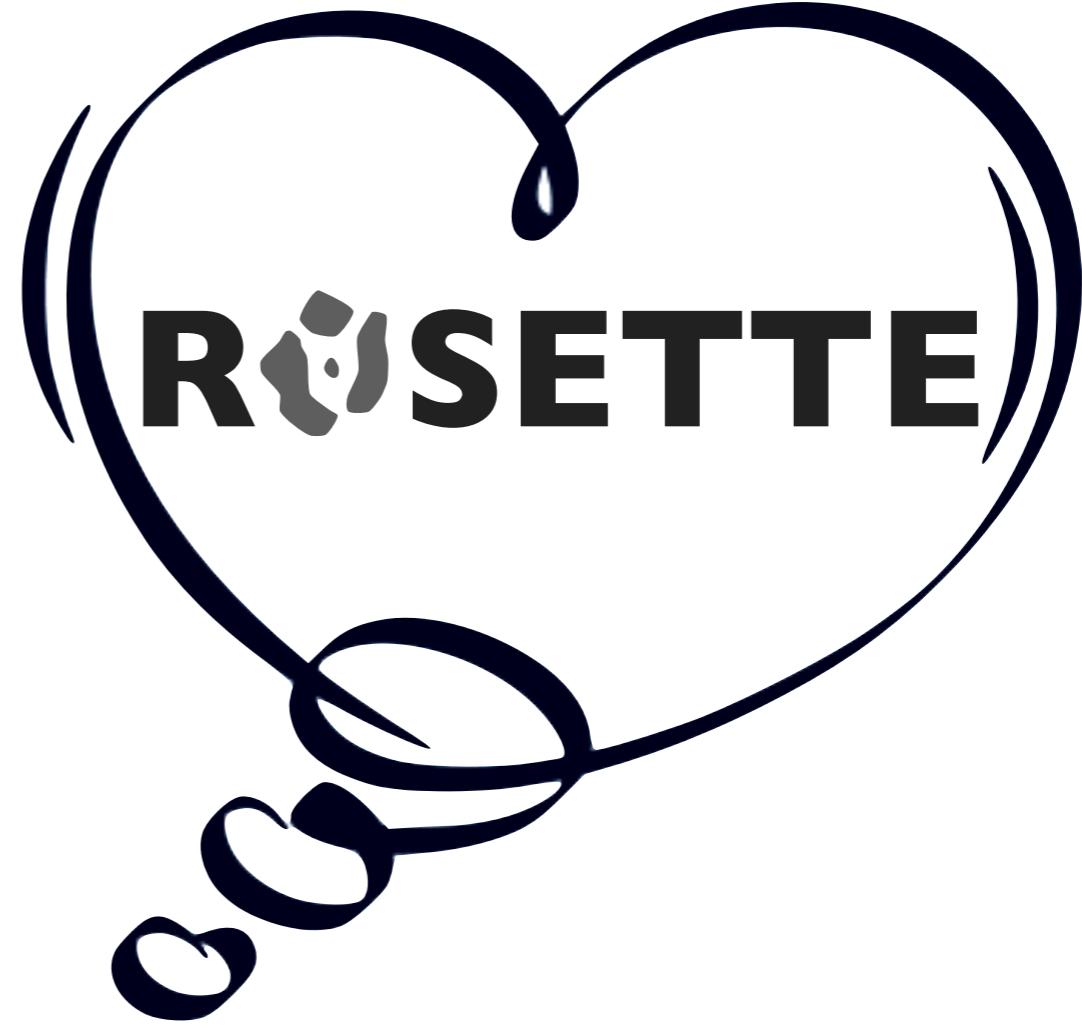
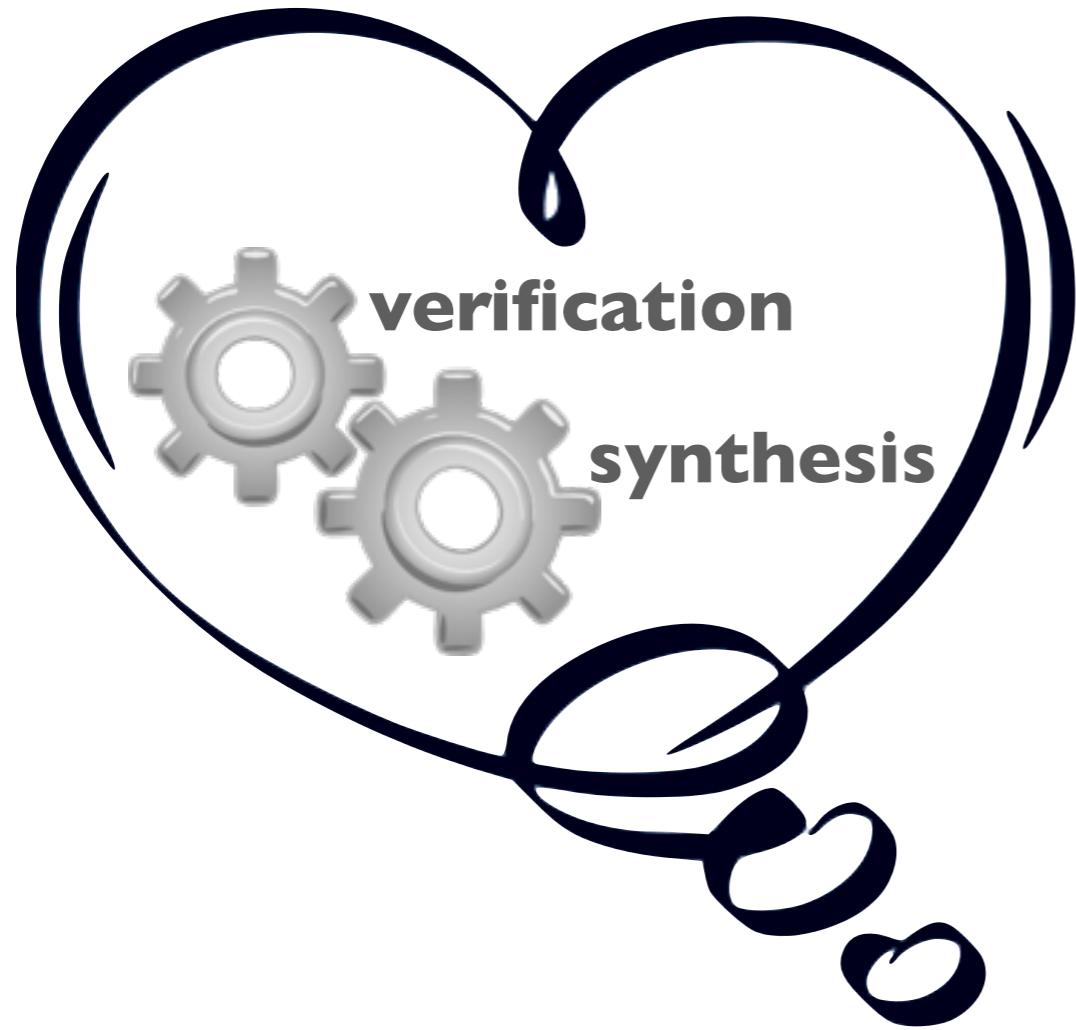
**a programmable programming language**



**a programmable programming language**



**a programmable programming language**



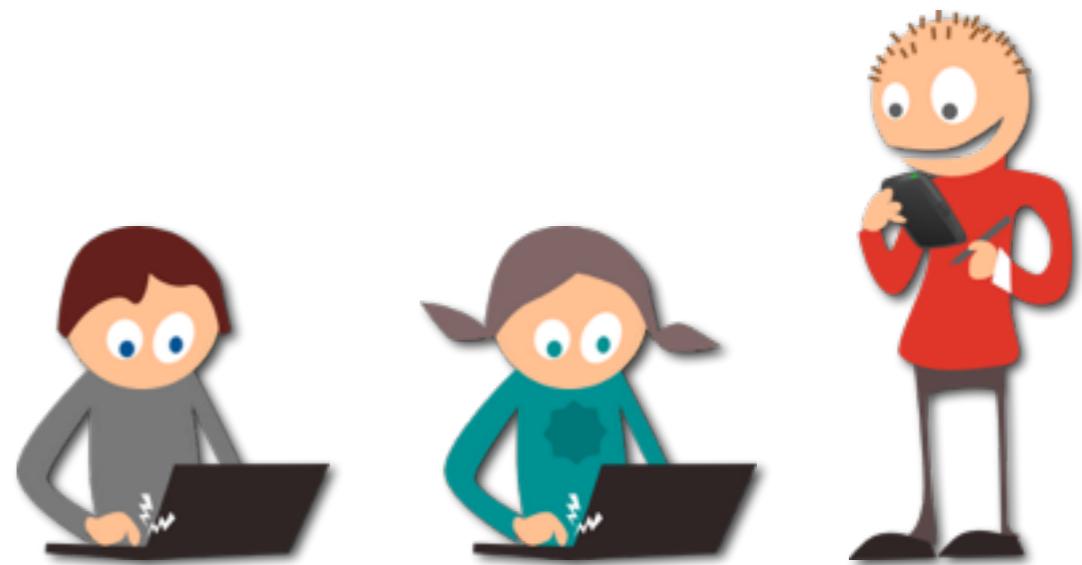
a **solver-aided** programming language



**a little programming for everyone**

# A little programming for **everyone**

Every knowledge worker wants to program ...



# A little programming for **everyone**

Every knowledge worker wants to program ...

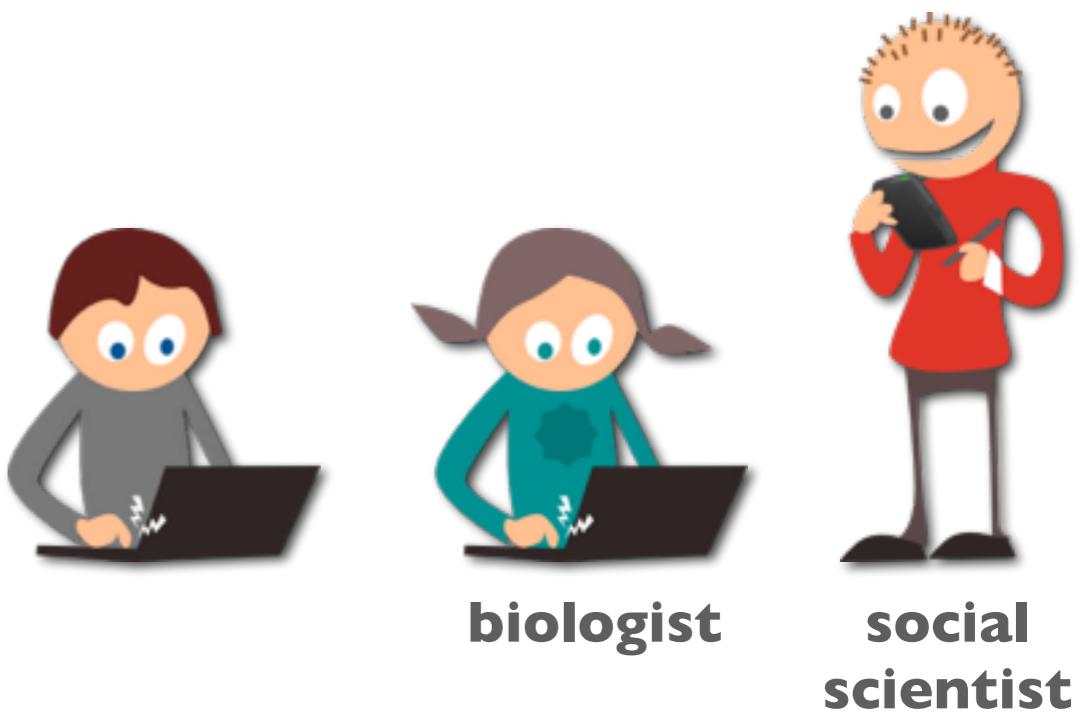
- spreadsheet data manipulation



# A little programming for everyone

Every knowledge worker wants to program ...

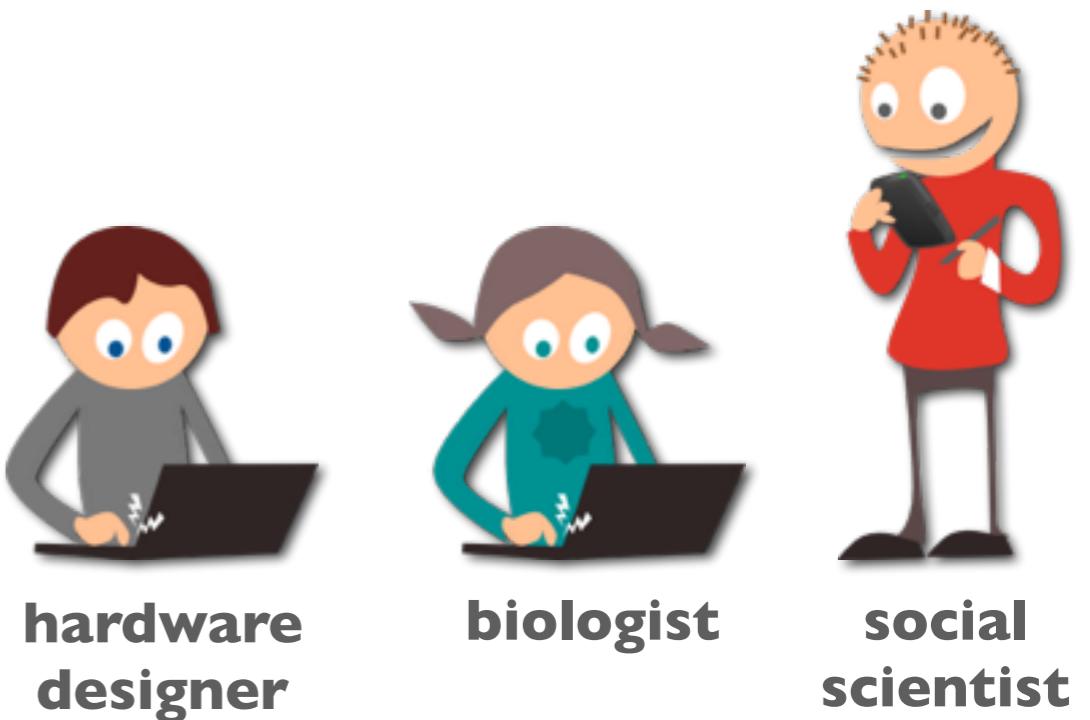
- spreadsheet data manipulation
- models of cell fates



# A little programming for everyone

Every knowledge worker wants to program ...

- spreadsheet data manipulation
- models of cell fates
- cache coherence protocols
- memory models



# A little programming for everyone

Every knowledge worker wants to program ...

- spreadsheet data manipulation
- models of cell fates
- cache coherence protocols
- memory models



**hardware  
designer**



**biologist**

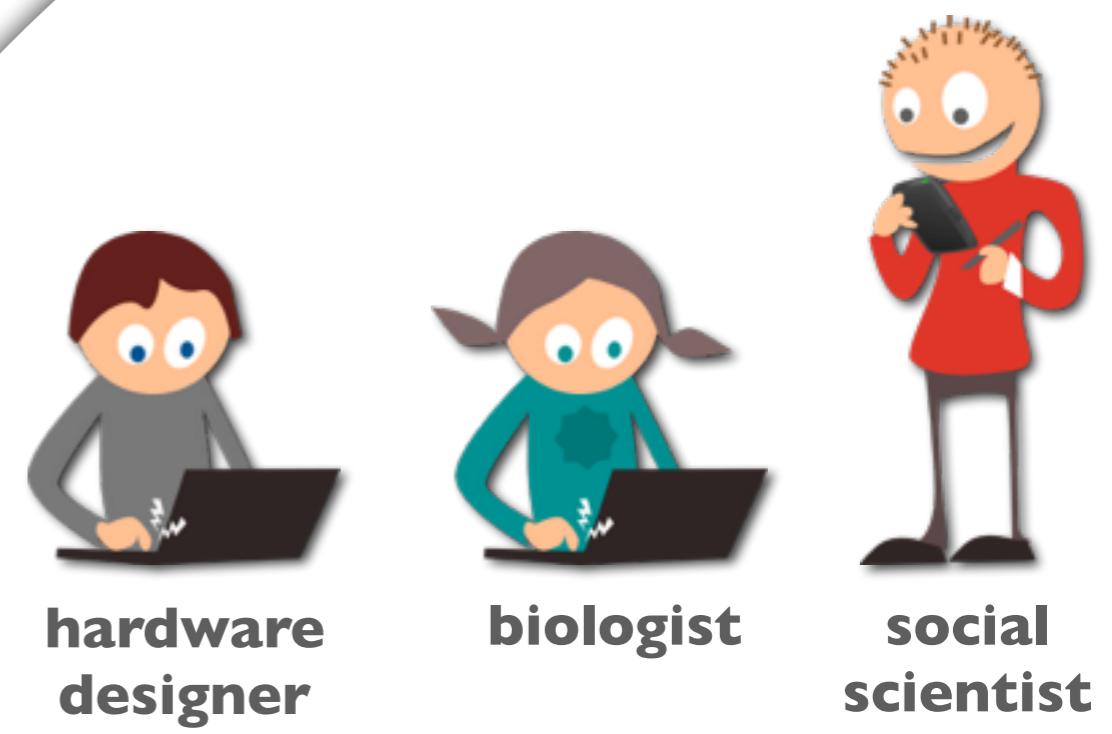
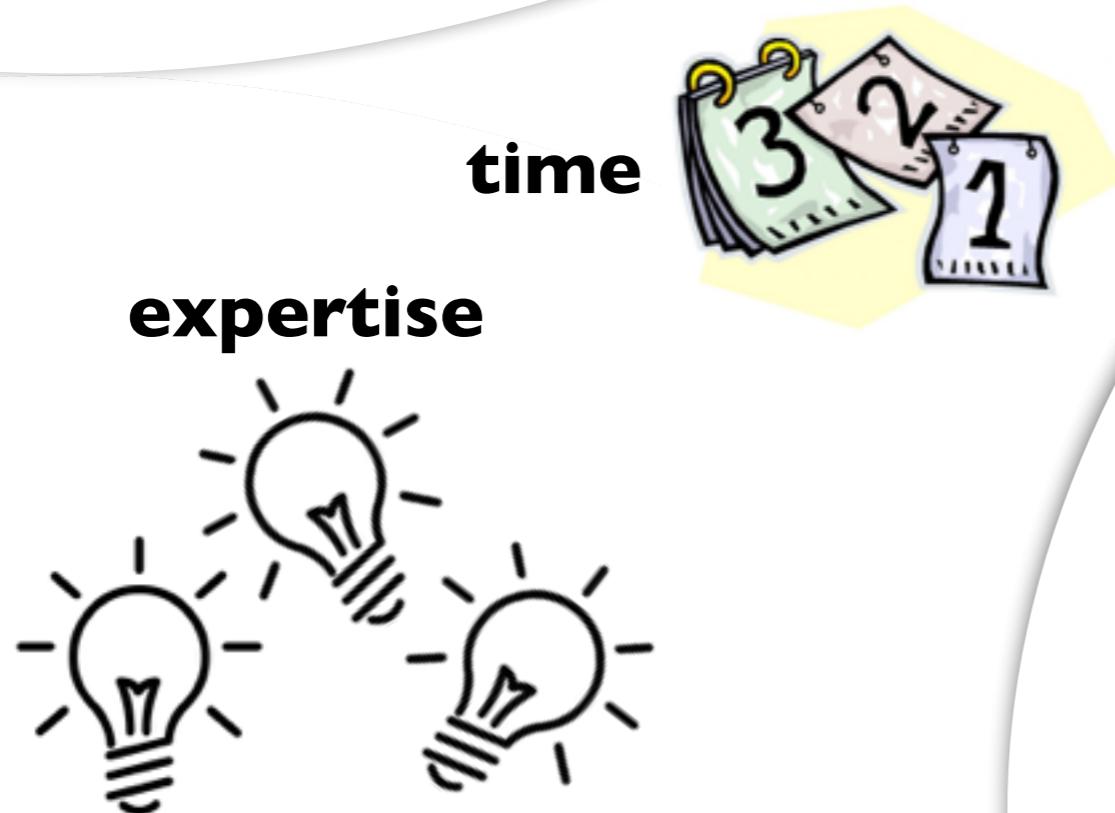


**social  
scientist**

# A little programming for everyone

Every knowledge worker wants to program ...

- spreadsheet data manipulation [Flashfill, POPL'11]
- models of cell fates [SBL, POPL'13]
- cache coherence protocols [Transit, PLDI'13]
- memory models [MemSAT, PLDI'10]

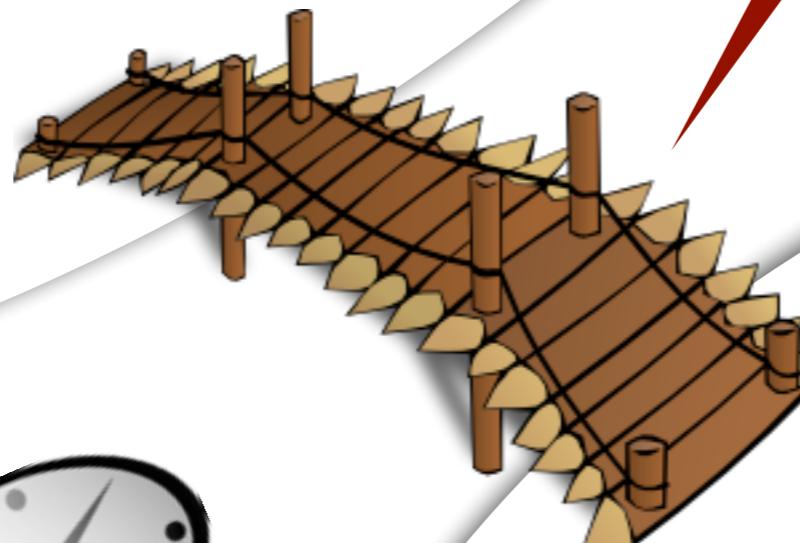


# A little programming for everyone

We all want to build programs ...

- spreadsheet data manipulation
- models of cell fates
- cache coherence protocols
- memory models

**solver-aided languages**



**less time**



**less expertise**



**hardware  
designer**



**biologist**



**social  
scientist**



**solver-aided tools**

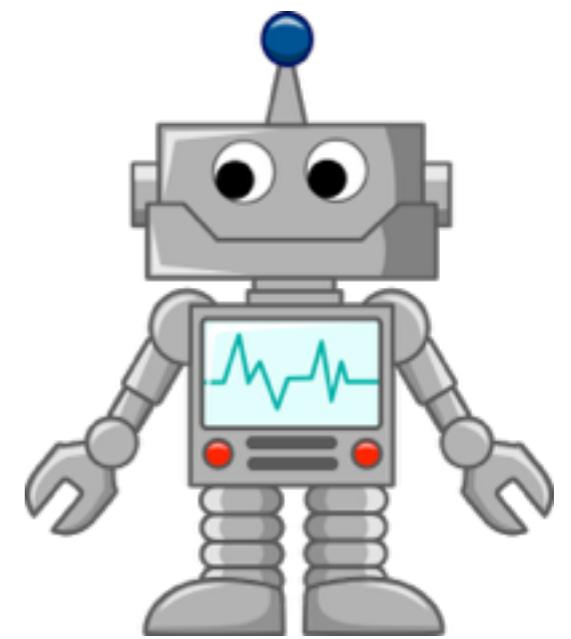


outline

**solver-aided tools, languages**



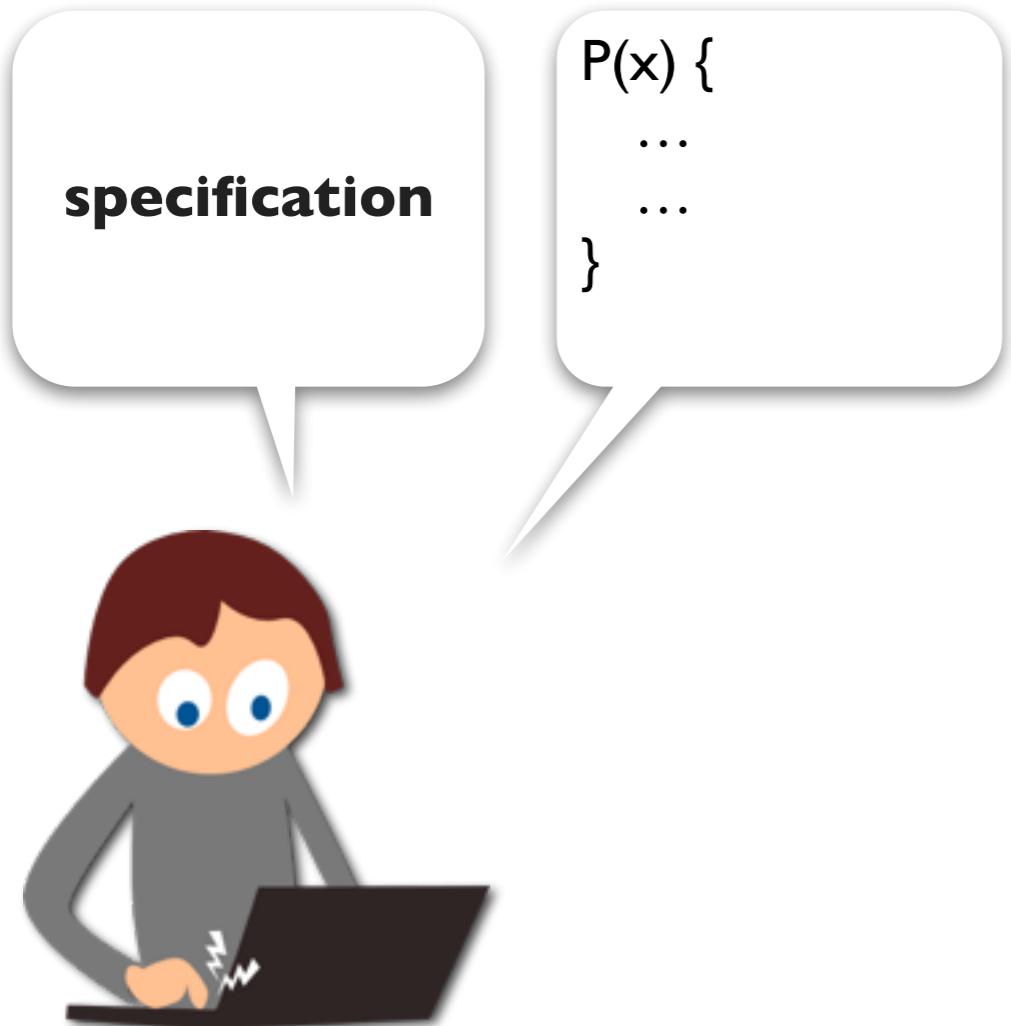
# **solver-aided tools, languages, and applications**



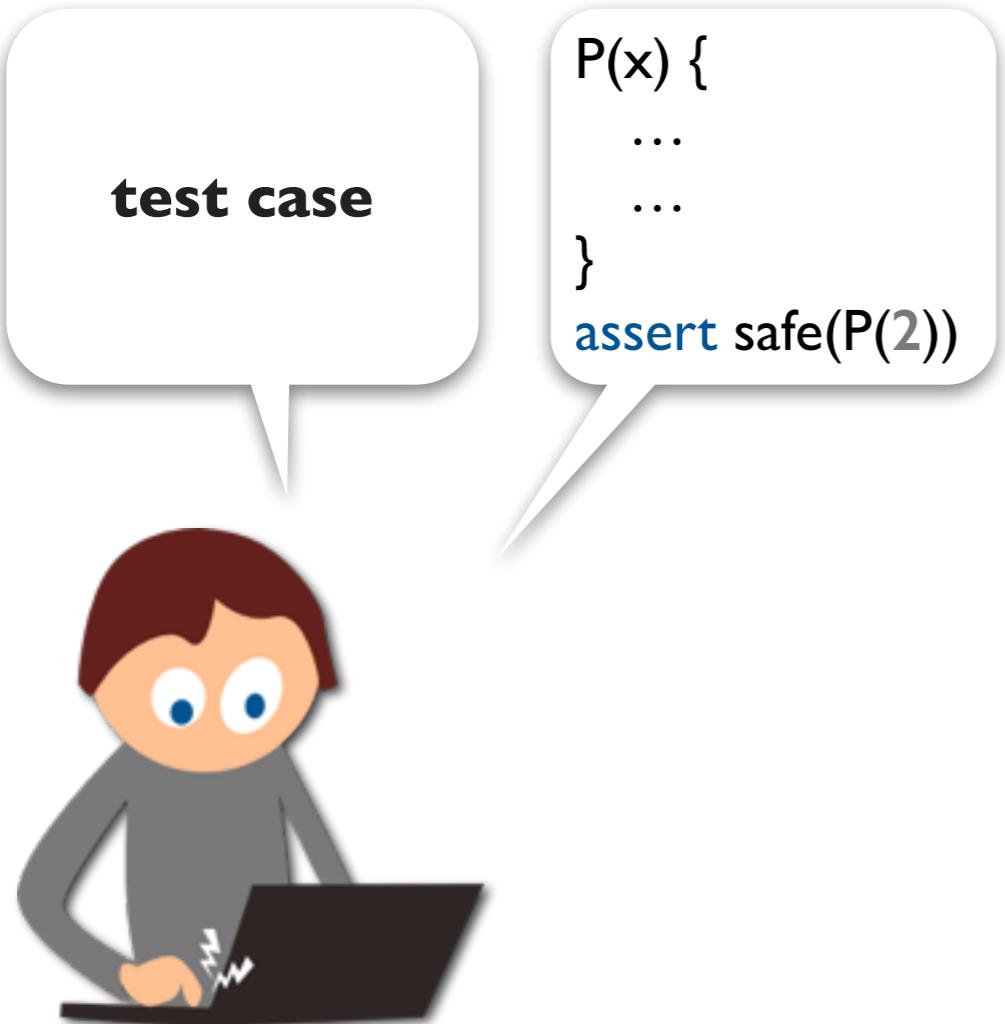
**solver-aided tools**



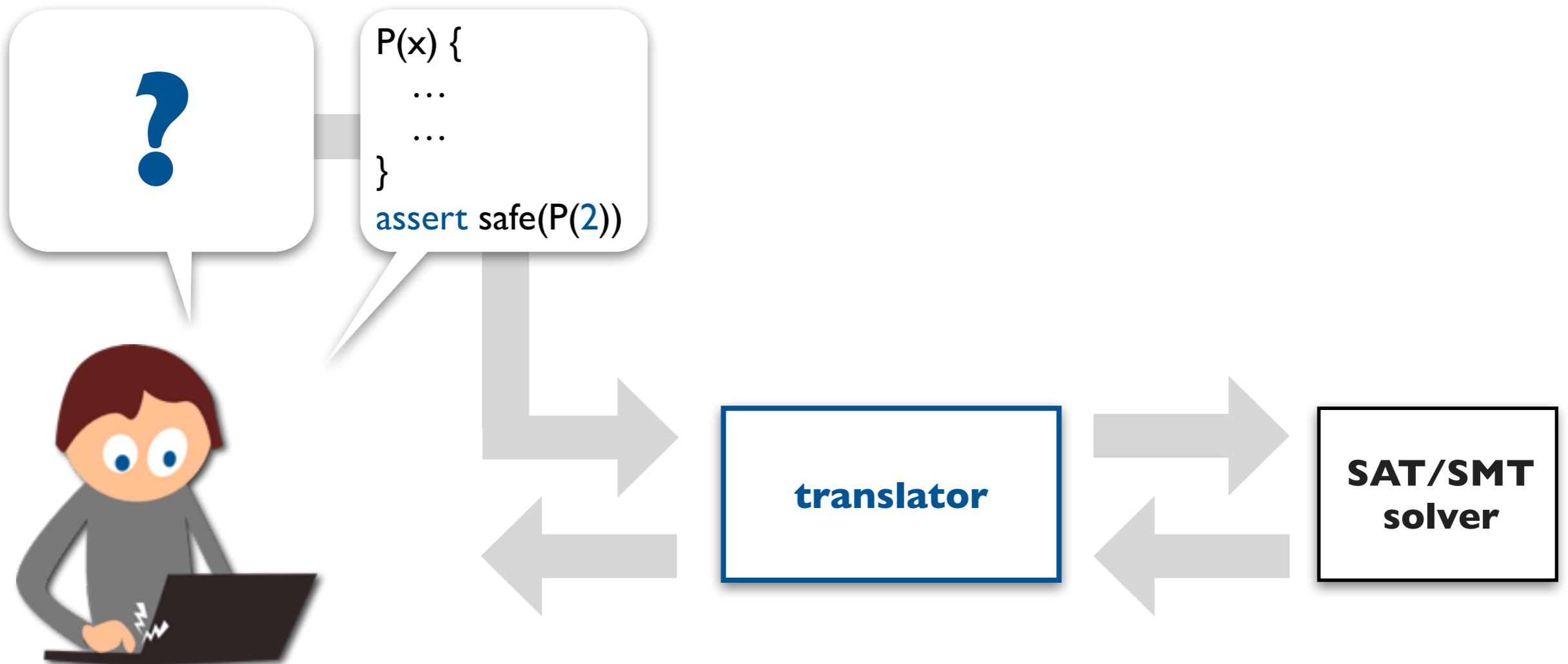
# Programming ...



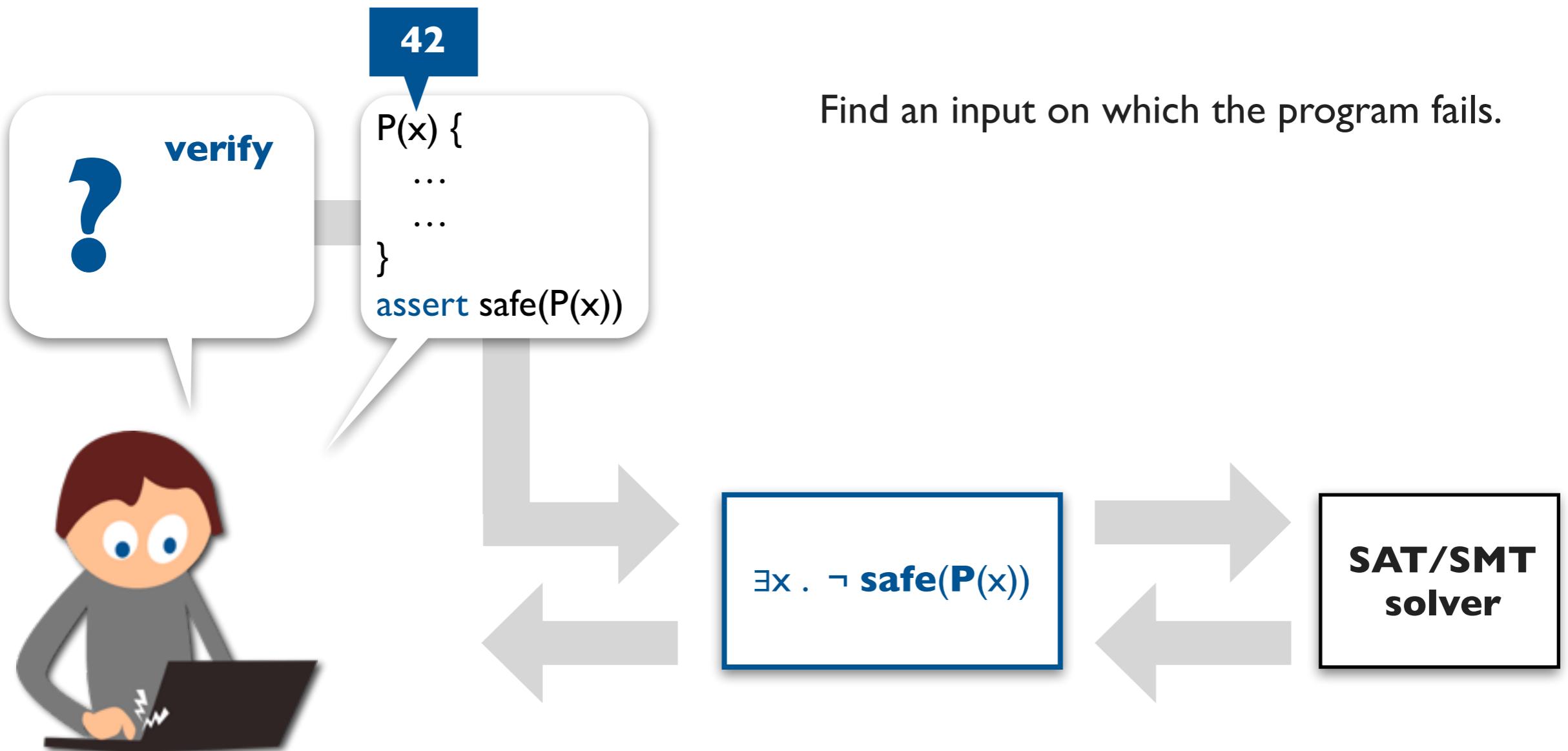
# Programming ...



# Programming with a solver-aided tool



# Programming with a solver-aided tool



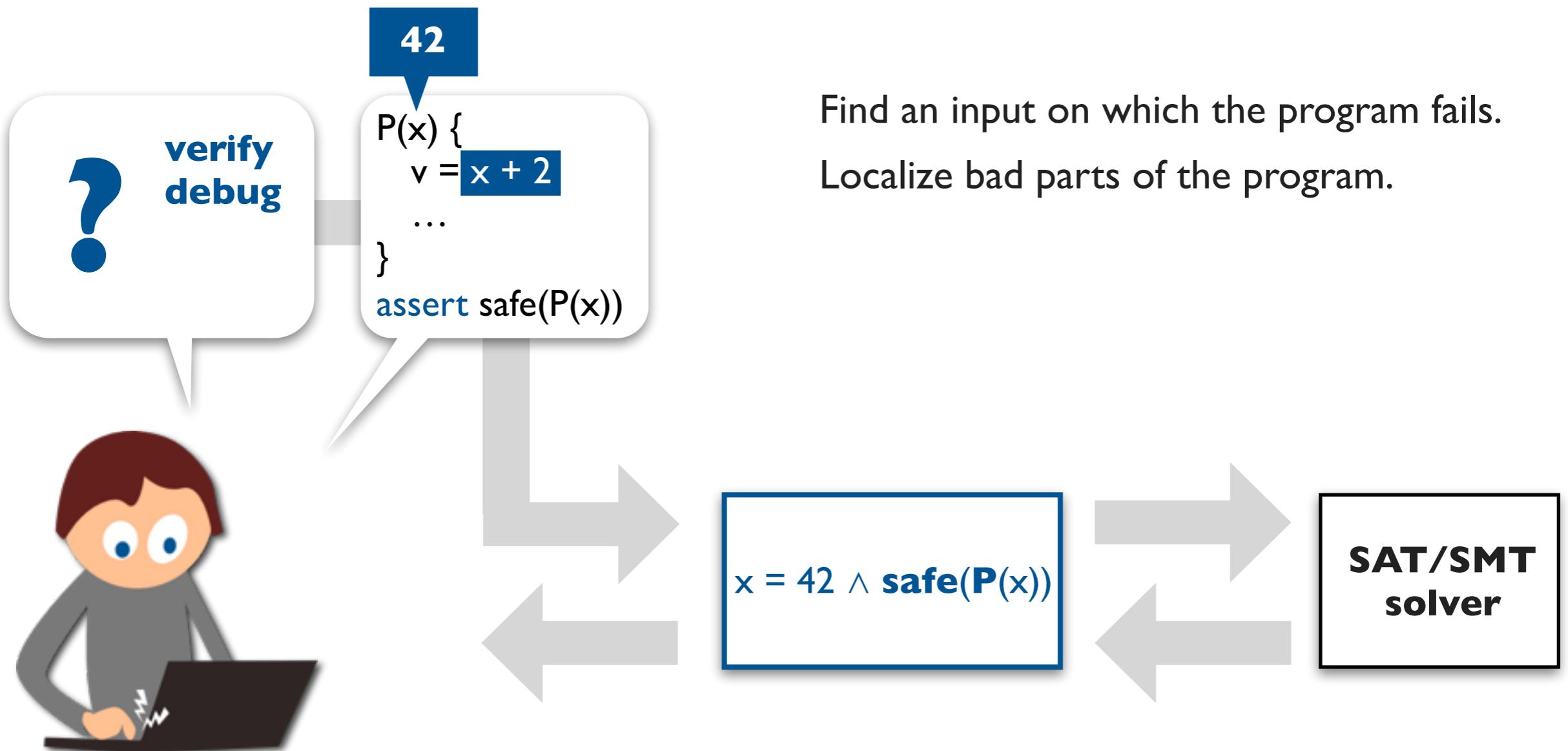
CBMC [Kroening et al., DAC'03]

Dafny [Leino, LPAR'10]

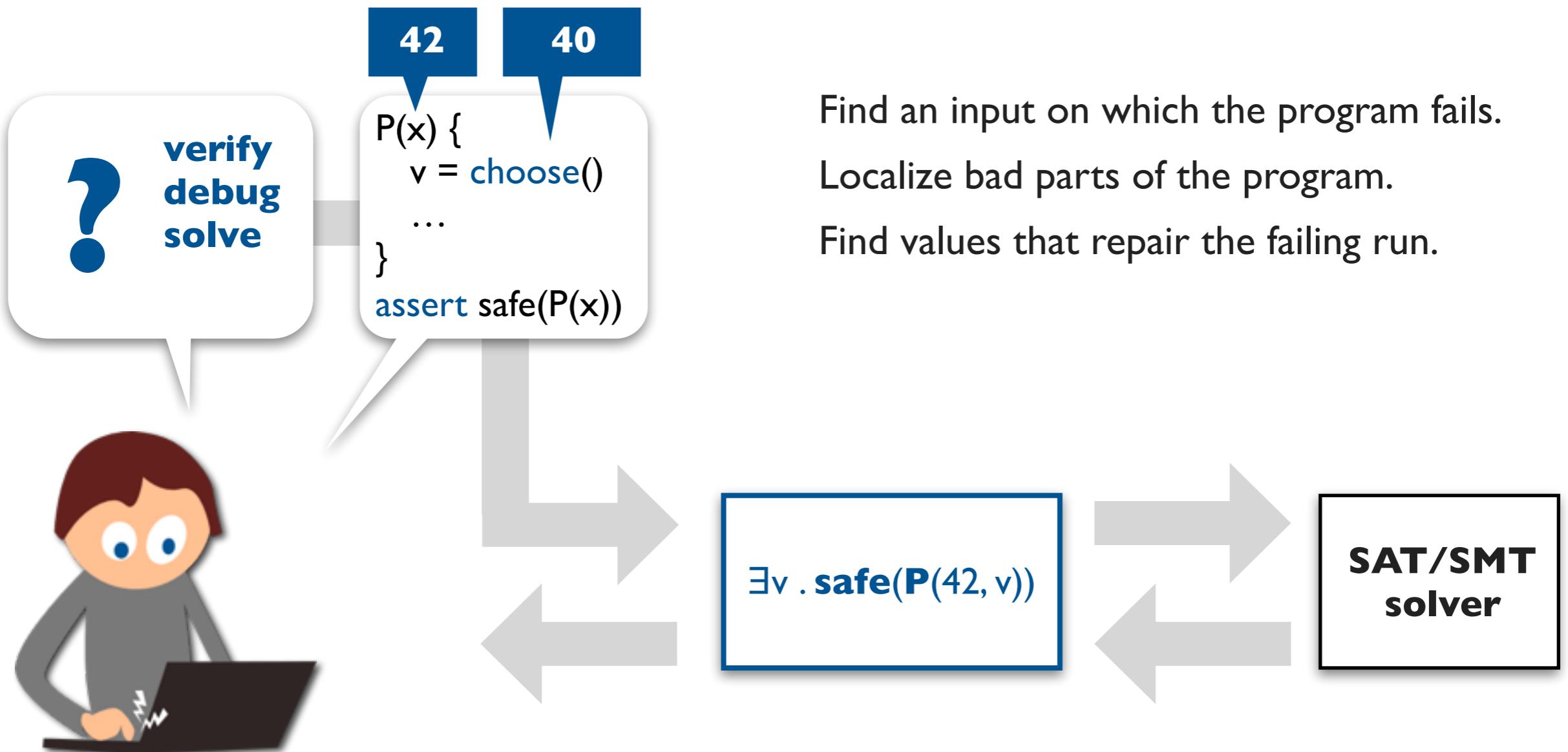
Miniatur [Vaziri et al., FSE'07]

Klee [Cadar et al., OSDI'08]

# Programming with a solver-aided tool



# Programming with a solver-aided tool

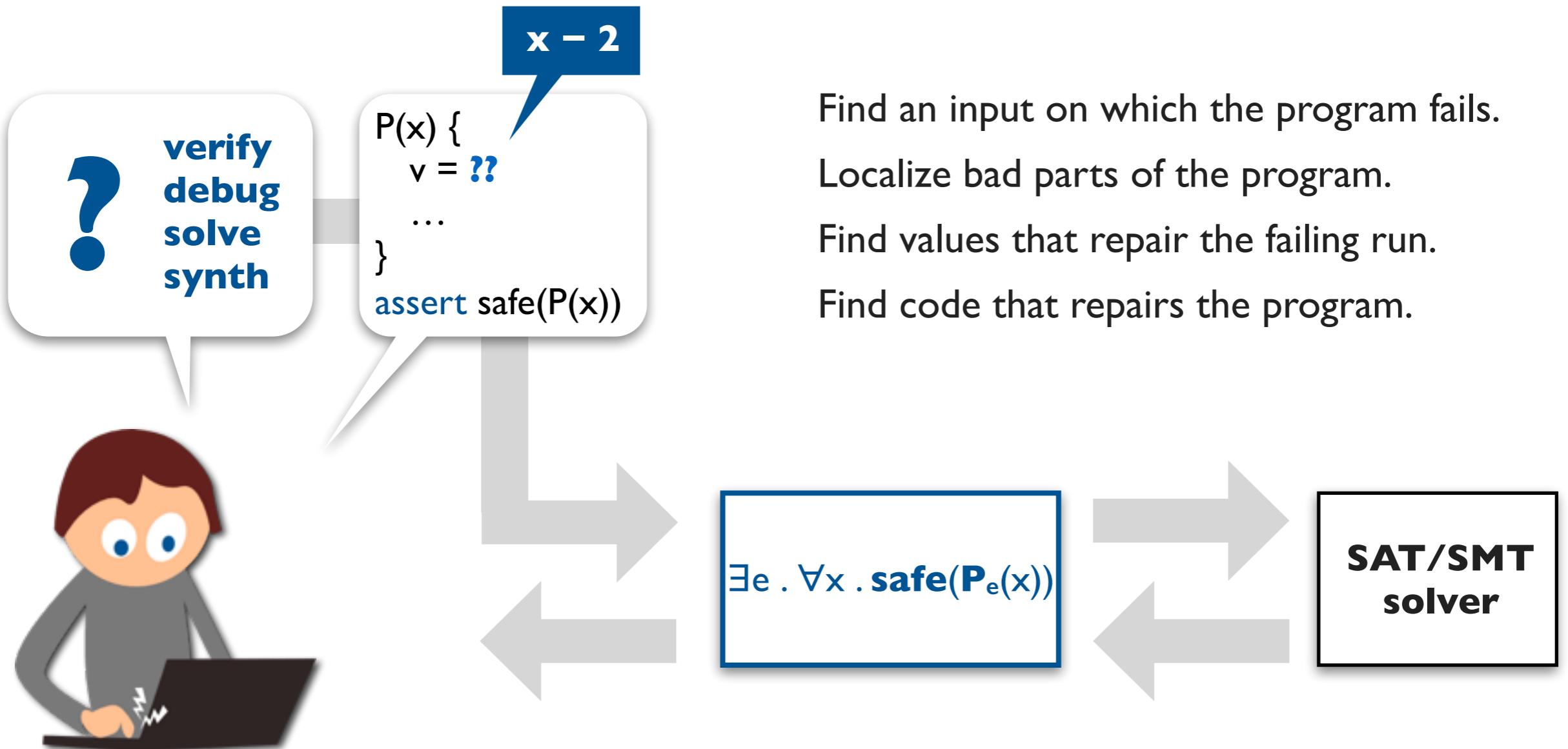


Kaplan [Koksal et al, POPL'12]

PBnJ [Samimi et al., ECOOP'10]

Squander [Milicevic et al., ICSE'11]

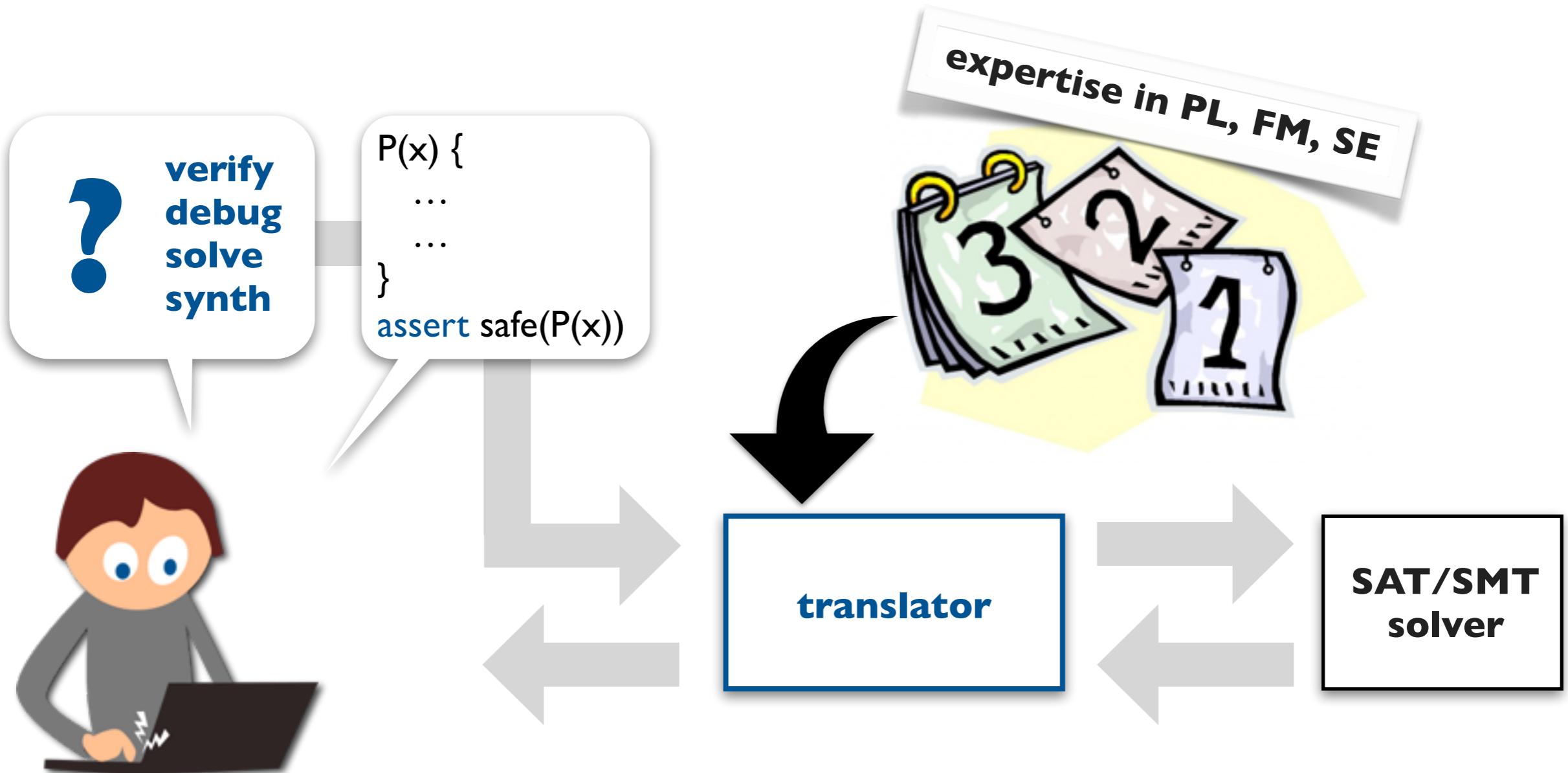
# Programming with a solver-aided tool



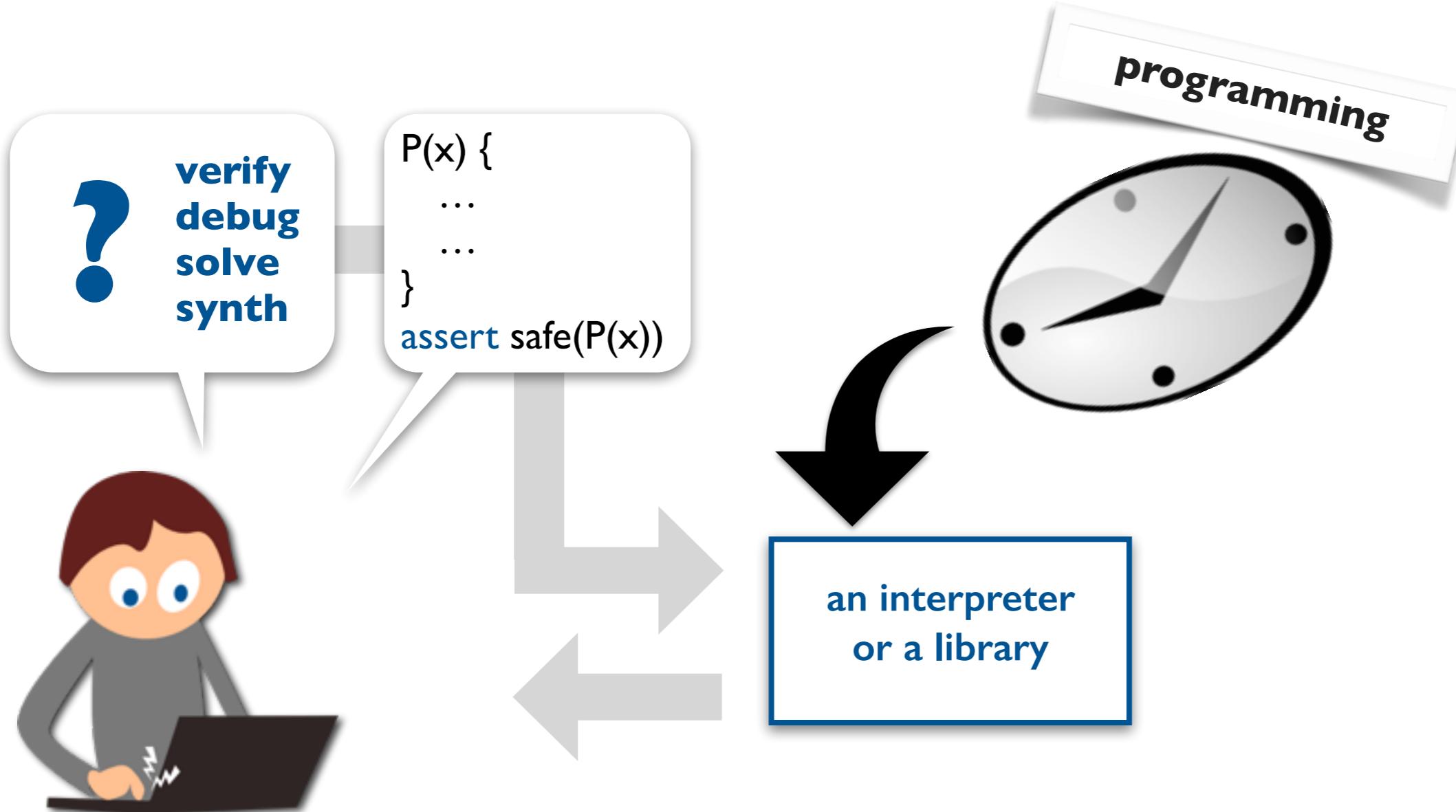
Sketch [Solar-Lezama et al., ASPLOS'06]

Comfy [Kuncak et al., CAV'10]

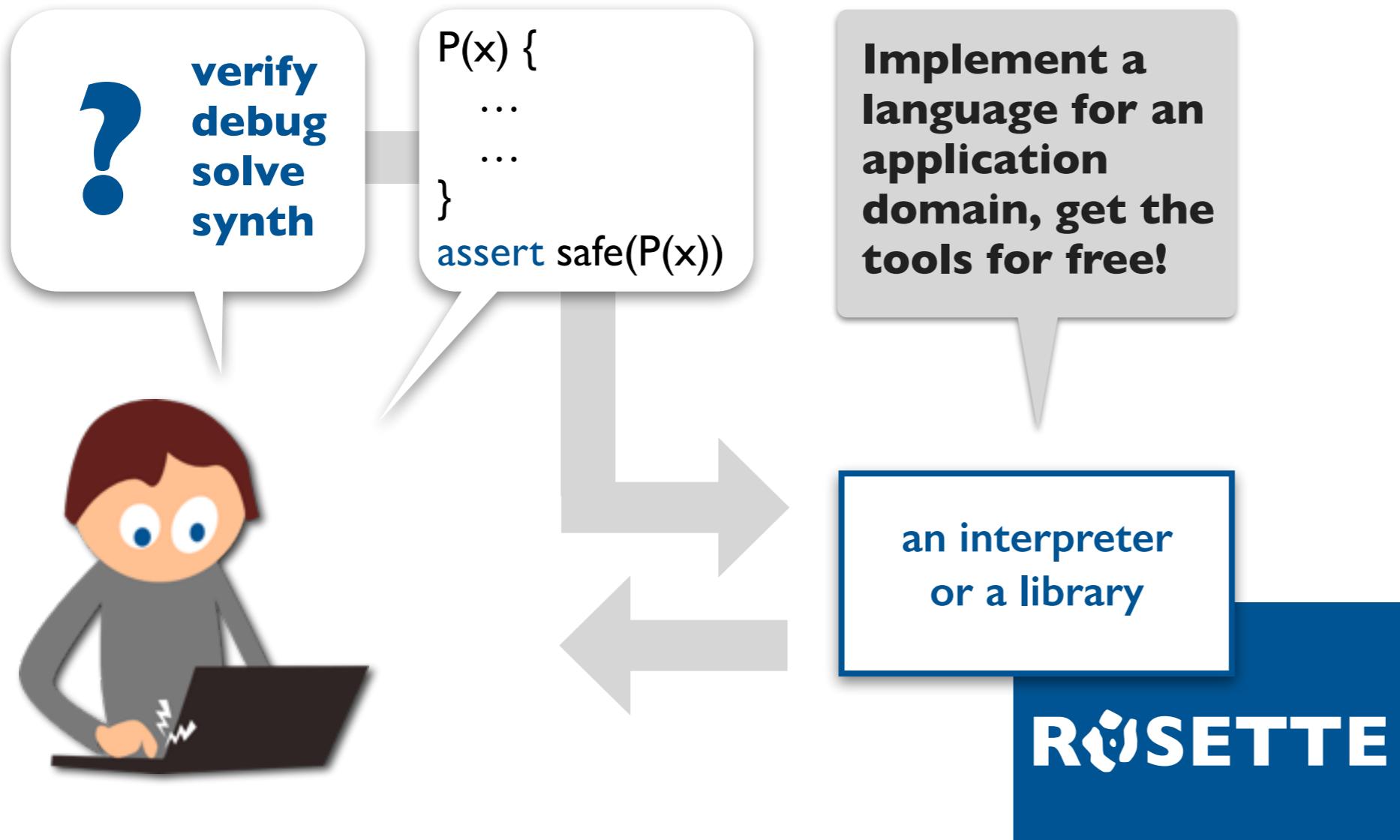
# The standard (hard) way to build a tool



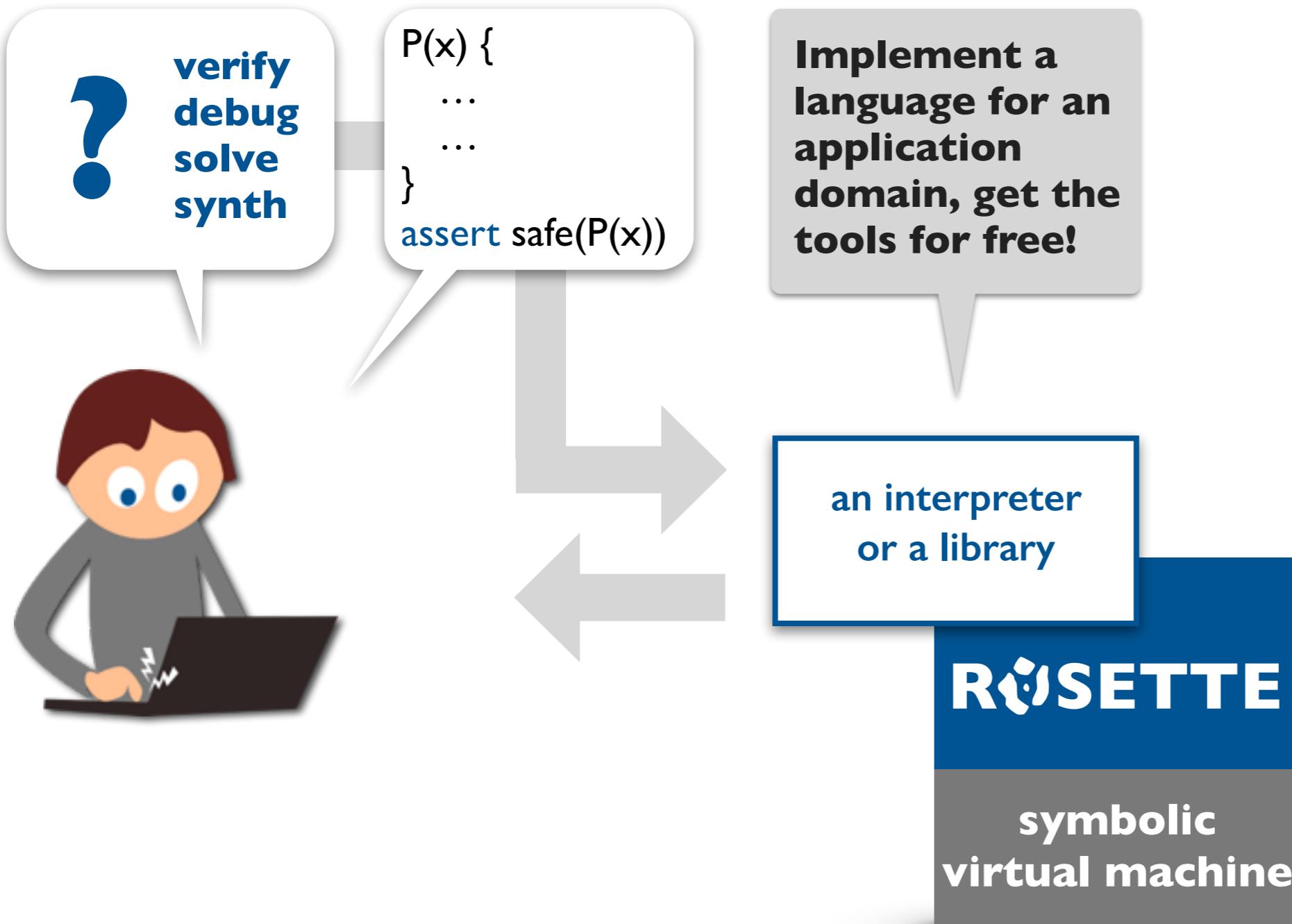
# A new, easy way to build tools



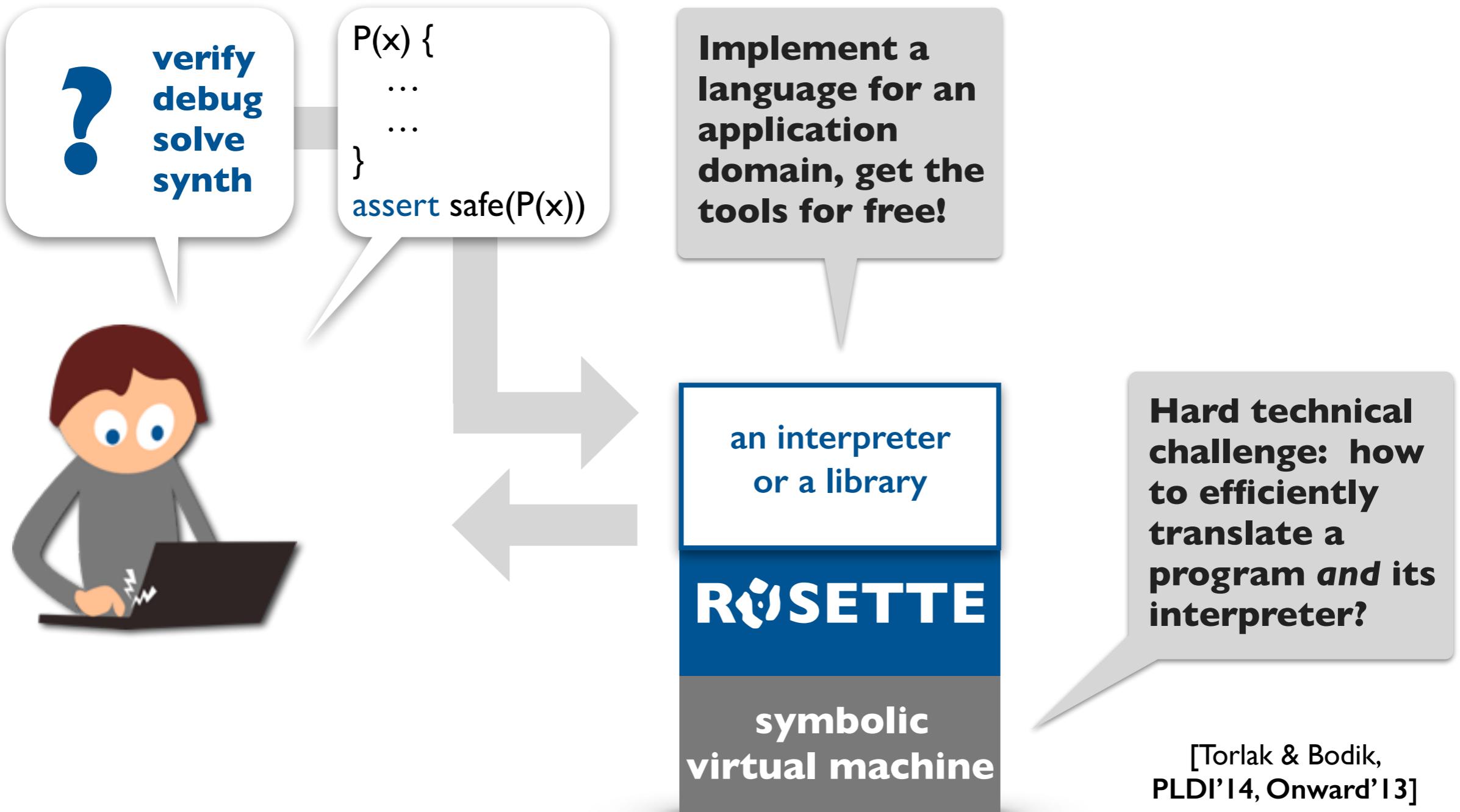
# A new, easy way to build tools



# A new, easy way to build tools



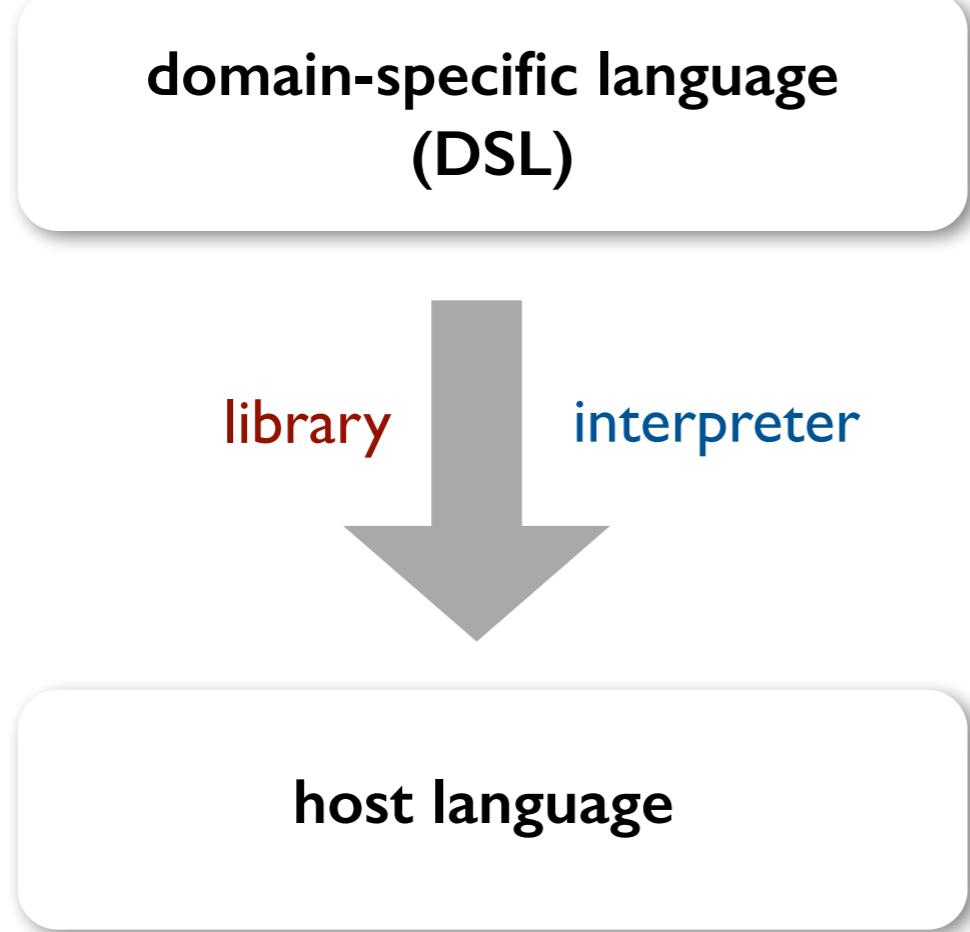
# A new, easy way to build tools



**design**  
**solver-aided languages**



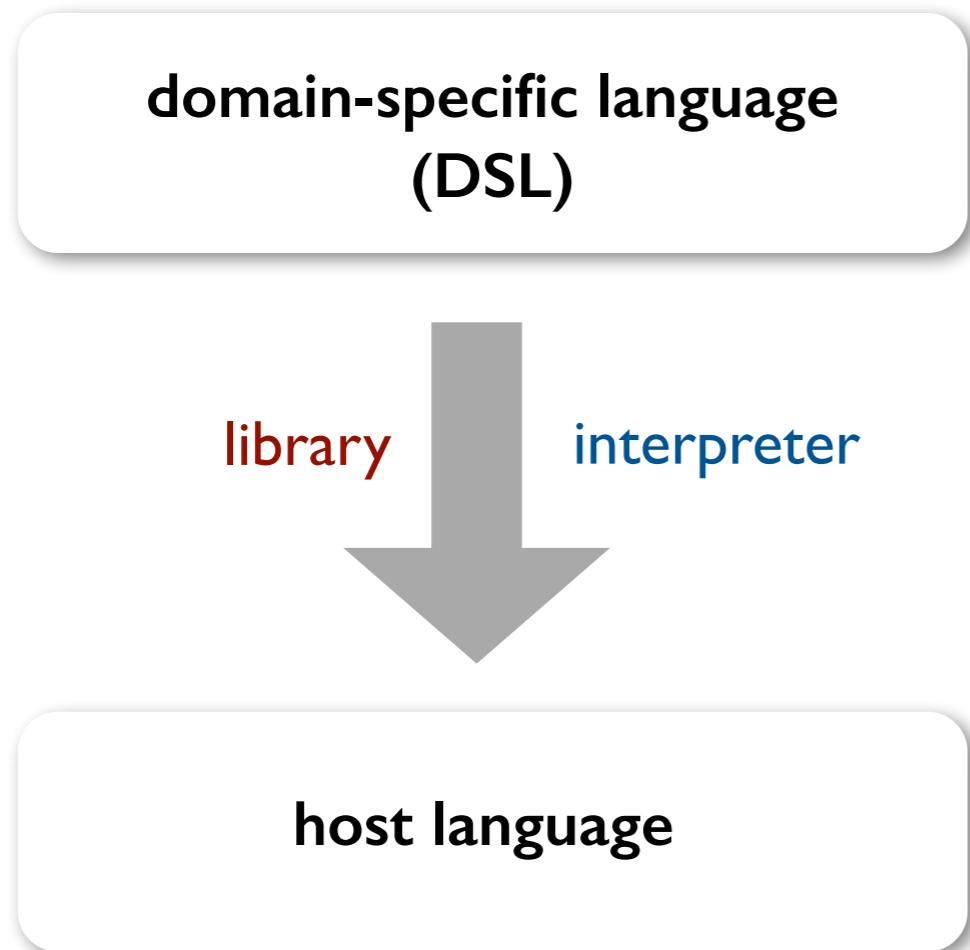
# Layers of languages



A formal language that is specialized to a particular application domain and often limited in capability.

A high-level language for implementing DSLs, usually with meta-programming features.

# Layers of languages



**artificial intelligence**

[Church](#), [BLOG](#)

**databases**

[SQL](#), [Datalog](#)

**hardware design**

[Bluespec](#), [Chisel](#), [Verilog](#), [VHDL](#)

**math and statistics**

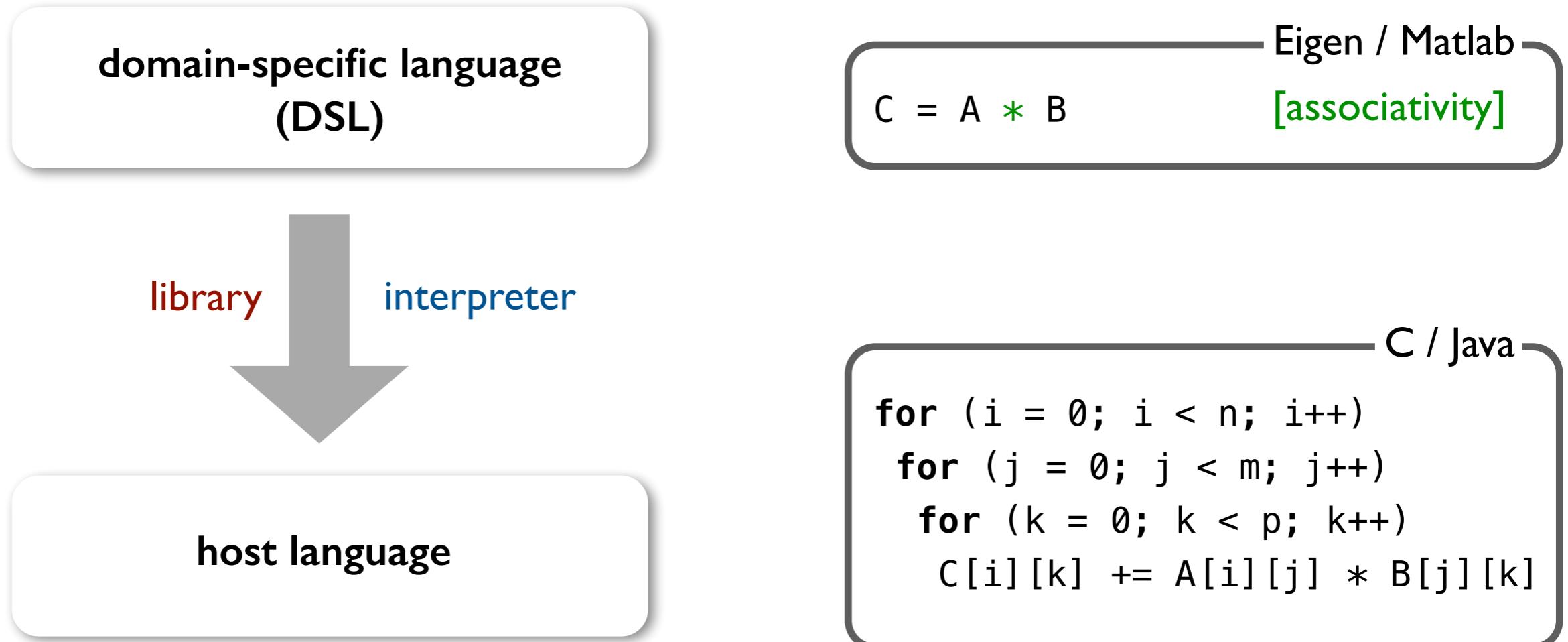
[Eigen](#), [Matlab](#), [R](#)

**layout and visualization**

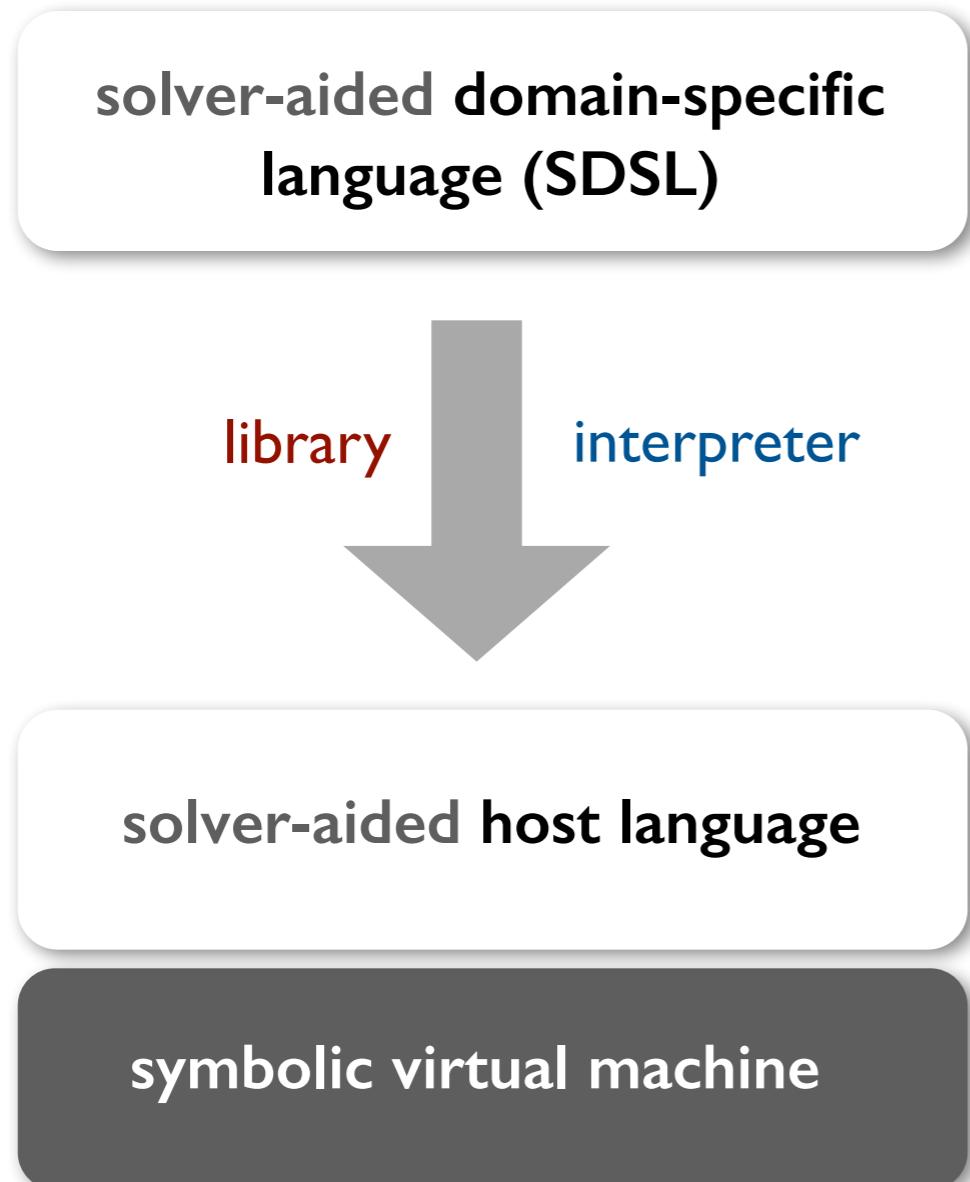
[LaTex](#), [dot](#), [dygraphs](#), [D3](#)

Scala, Racket, JavaScript

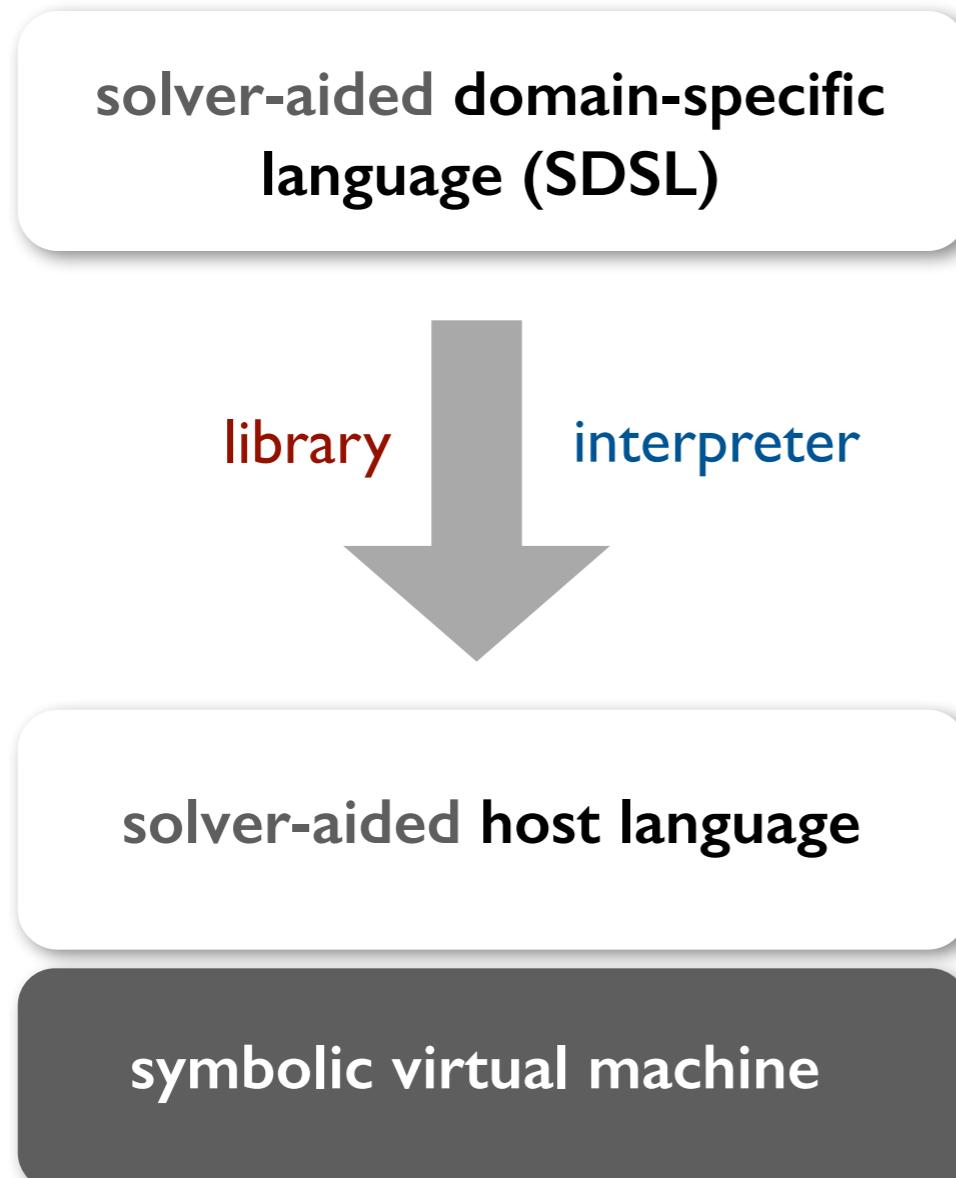
# Layers of languages



# Layers of solver-aided languages

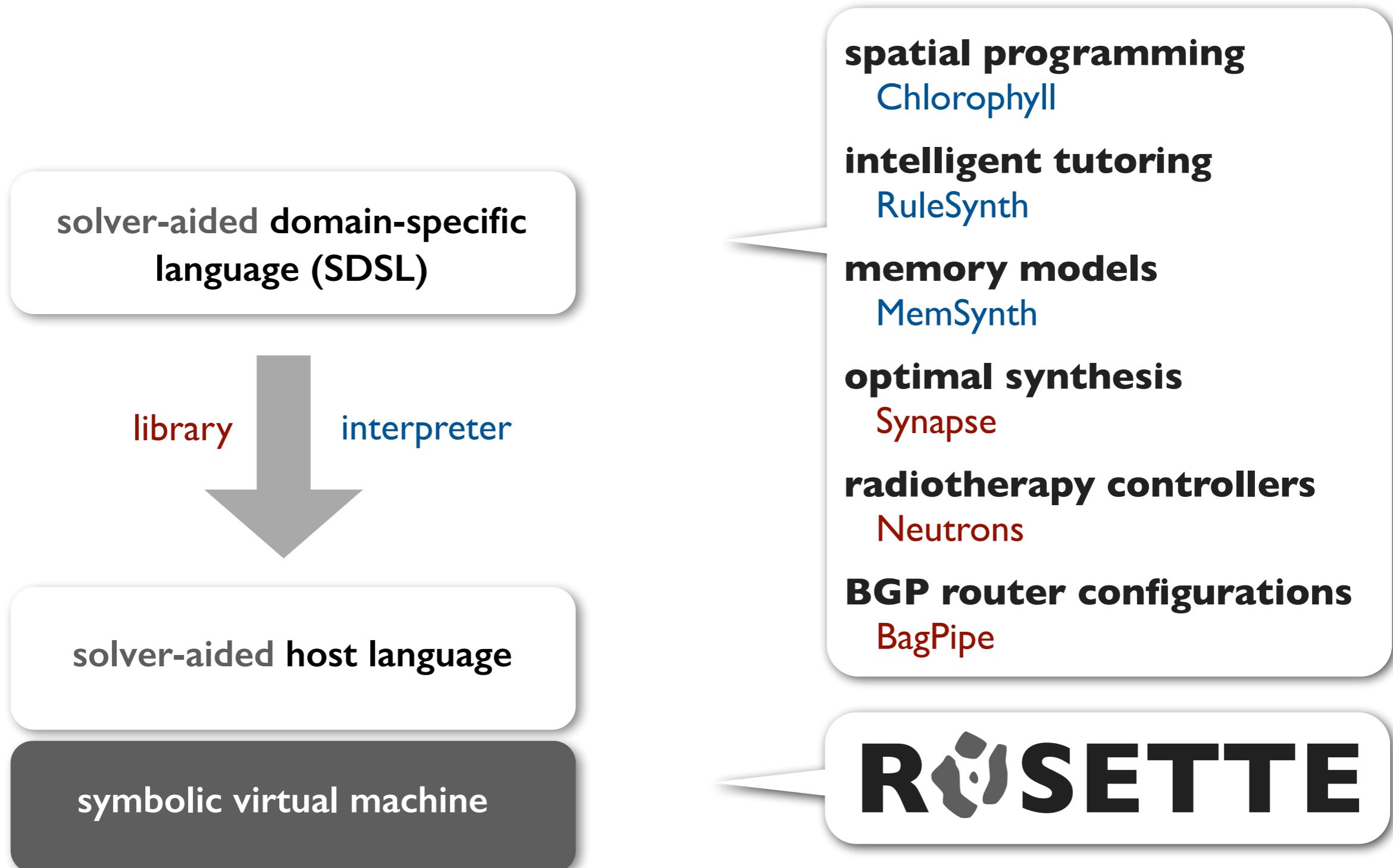


# Layers of solver-aided languages



[Torlak & Bodik, Onward'13, PLDI'14]

# Layers of solver-aided languages



[Torlak & Bodik, Onward'13, PLDI'14]

# **Anatomy of a solver-aided host language**



# Racket

# Anatomy of a solver-aided host language

```
(define-symbolic id type)  
(assert expr)  
(verify expr)  
(debug [expr] expr)  
(solve expr)  
(synthesize [expr] expr)
```



# A tiny example SDSL

```
def bvmax(r0, r1) :  
    r2 = bvge(r0, r1)  
    r3 = bvneg(r2)  
    r4 = bvxor(r0, r2)  
    r5 = bvand(r3, r4)  
    r6 = bvxor(r1, r5)  
    return r6
```

**BV**: A tiny assembly-like language for writing fast, low-level library functions.

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test    debug  
verify    synth

BV: A tiny assembly-like language for writing fast, low-level library functions.

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```

test    debug  
verify    synth

BV: A tiny assembly-like language for writing fast, low-level library functions.

1. interpreter [10 LOC]
2. verifier [free]
3. debugger [free]
4. synthesizer [free]

# A tiny example SDSL: ROSETTE

```
def bvmax(r0, r1) :  
    r2 = bvge(r0, r1)  
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> bvmax(-2, -1)
```

# A tiny example SDSL:



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parse

```
(define bvmax  
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```

(out opcode in ...)

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```
> bvmax(-2, -1)
```

interpret

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```

`(-2 -1)

```
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  (make-registers prog inputs)  
  (for ([stmt prog])  
    (match stmt  
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0	-2
1	-1
2	
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4	
5	
6	

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```

- pattern matching
- dynamic evaluation
- first-class & higher-order procedures
- side effects

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    r5 = bvand(r3, r4)  
    r6 = bvxor(r1, r5)  
    return r6  
  
> verify(bvmax, max)
```

query

```
(define-symbolic n0 n1 integer?)  
(define inputs (list n0 n1))  
(verify  
  (assert (= (interpret bvmax inputs)  
            (interpret max inputs))))
```

# A tiny example SDSL:



```
def bvmax(r0, r1) :  
    r2 = bvge(r0, r1)  
    r3 = bvneg(r2)  
    r4 = bvxor(r0, r2)  
    r5 = bvand(r3, r4)  
    r6 = bvxor(r1, r5)  
    return r6  
  
> verify(bvmax, max)
```

query

Creates two fresh symbolic constants of type number and binds them to variables n0 and n1.

```
(define-symbolic n0 n1 integer?)  
(define inputs (list n0 n1))  
(verify  
  (assert (= (interpret bvmax inputs)  
            (interpret max inputs))))
```

# A tiny example SDSL:



```
def bvmax(r0, r1) :  
    r2 = bvge(r0, r1)  
    r3 = bvneg(r2)  
    r4 = bvxor(r0, r2)  
    r5 = bvand(r3, r4)  
    r6 = bvxor(r1, r5)  
    return r6  
  
> verify(bvmax, max)
```

query

Symbolic values can be used just like concrete values of the same type.

```
(define-symbolic n0 n1 integer?)  
(define inputs (list n0 n1))  
(verify  
  (assert (= (interpret bvmax inputs)  
            (interpret max inputs))))
```

# A tiny example SDSL:



```
def bvmax(r0, r1) :  
    r2 = bvge(r0, r1)  
    r3 = bvneg(r2)  
    r4 = bvxor(r0, r2)  
    r5 = bvand(r3, r4)  
    r6 = bvxor(r1, r5)  
    return r6
```

```
> verify(bvmax, max)  
(0, -2)
```

query

```
(define-symbolic n0 n1 integer?)  
(define inputs (list n0 n1))  
(verify  
  (assert (= (interpret bvmax inputs)  
            (interpret max inputs))))
```

(**verify** *expr*) searches for a concrete interpretation of symbolic constants that causes *expr* to fail.

# A tiny example SDSL:



```
def bvmax(r0, r1) :  
    r2 = bvge(r0, r1)  
    r3 = bvneg(r2)  
    r4 = bvxor(r0, r2)  
    r5 = bvand(r3, r4)  
    r6 = bvxor(r1, r5)  
    return r6
```

```
> verify(bvmax, max)  
(0, -2)
```

```
> bvmax(0, -2)  
-1
```

query

```
(define-symbolic n0 n1 integer?)  
(define inputs (list n0 n1))  
(verify  
  (assert (= (interpret bvmax inputs)  
            (interpret max inputs))))
```

# A tiny example SDSL:



```
def bvmax(r0, r1) :  
    r2 = bvge(r0, r1)  
    r3 = bvneg(r2)  
    r4 = bvxor(r0, r2)  
    r5 = bvand(r3, r4)  
    r6 = bvxor(r1, r5)  
    return r6
```

```
> debug(bvmax, max, (0, -2))
```

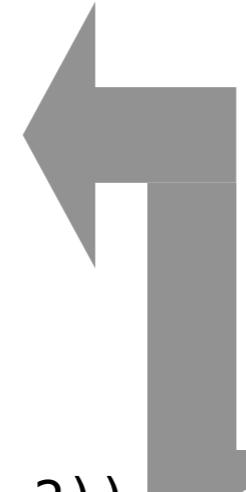
query

```
(define inputs (list 0 -2))  
(debug [input-register?])  
(assert (= (interpret bvmax inputs)  
          (interpret max inputs))))
```

# A tiny example SDSL:



```
def bvmax(r0, r1) :  
    r2 = bvge(r0, r1)  
    r3 = bvneg(r2)  
    r4 = bvxor(r0, r2)  
    r5 = bvand(r3, r4)  
    r6 = bvxor(r1, r5)  
    return r6  
  
> debug(bvmax, max, (0, -2))
```



query

```
(define inputs (list 0 -2))  
(debug [input-register?])  
(assert (= (interpret bvmax inputs)  
          (interpret max inputs))))
```

# A tiny example SDSL:



```
def bvmax(r0, r1) :  
    r2 = bvge(r0, r1)  
    r3 = bvneg(r2)  
    r4 = bvxor(??, ??)  
    r5 = bvand(r3, ??)  
    r6 = bvxor(??, ??)  
return r6  
  
> synthesize(bvmax, max)
```

query

```
(define-symbolic n0 n1 integer?)  
(define inputs (list n0 n1))  
(synthesize [inputs]  
  (assert (= (interpret bvmax inputs)  
            (interpret max inputs))))
```

# A tiny example SDSL:



```
def bvmax(r0, r1) :  
    r2 = bvge(r0, r1)  
    r3 = bvneg(r2)  
    r4 = bvxor(r0, r1)  
    r5 = bvand(r3, r4)  
    r6 = bvxor(r1, r5)  
return r6  
  
> synthesize(bvmax, max)
```



query

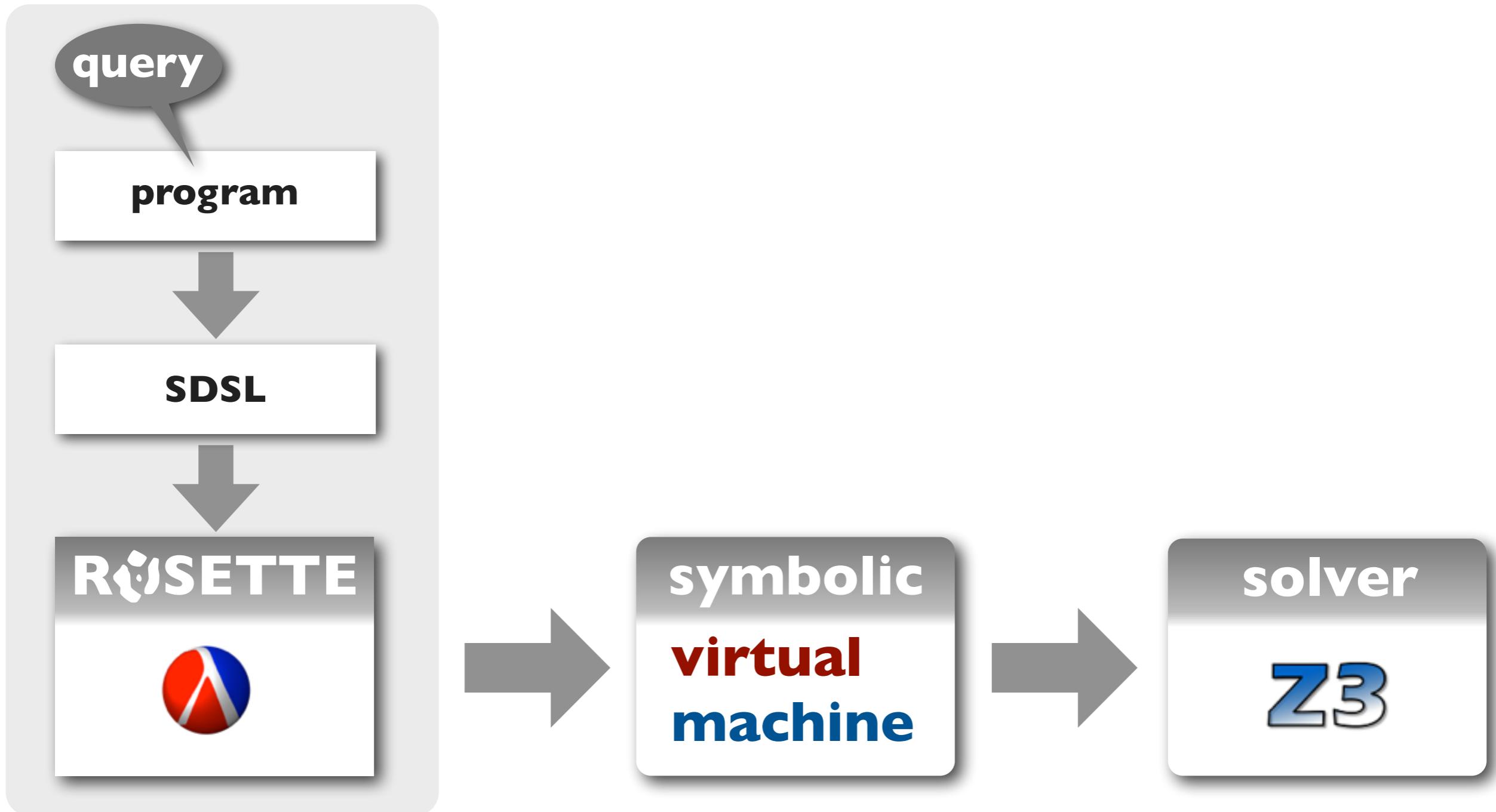
```
(define-symbolic n0 n1 integer?)  
(define inputs (list n0 n1))  
(synthesize [inputs]  
  (assert (= (interpret bvmax inputs)  
            (interpret max inputs)))))
```



**symbolic virtual machine (SVM)**



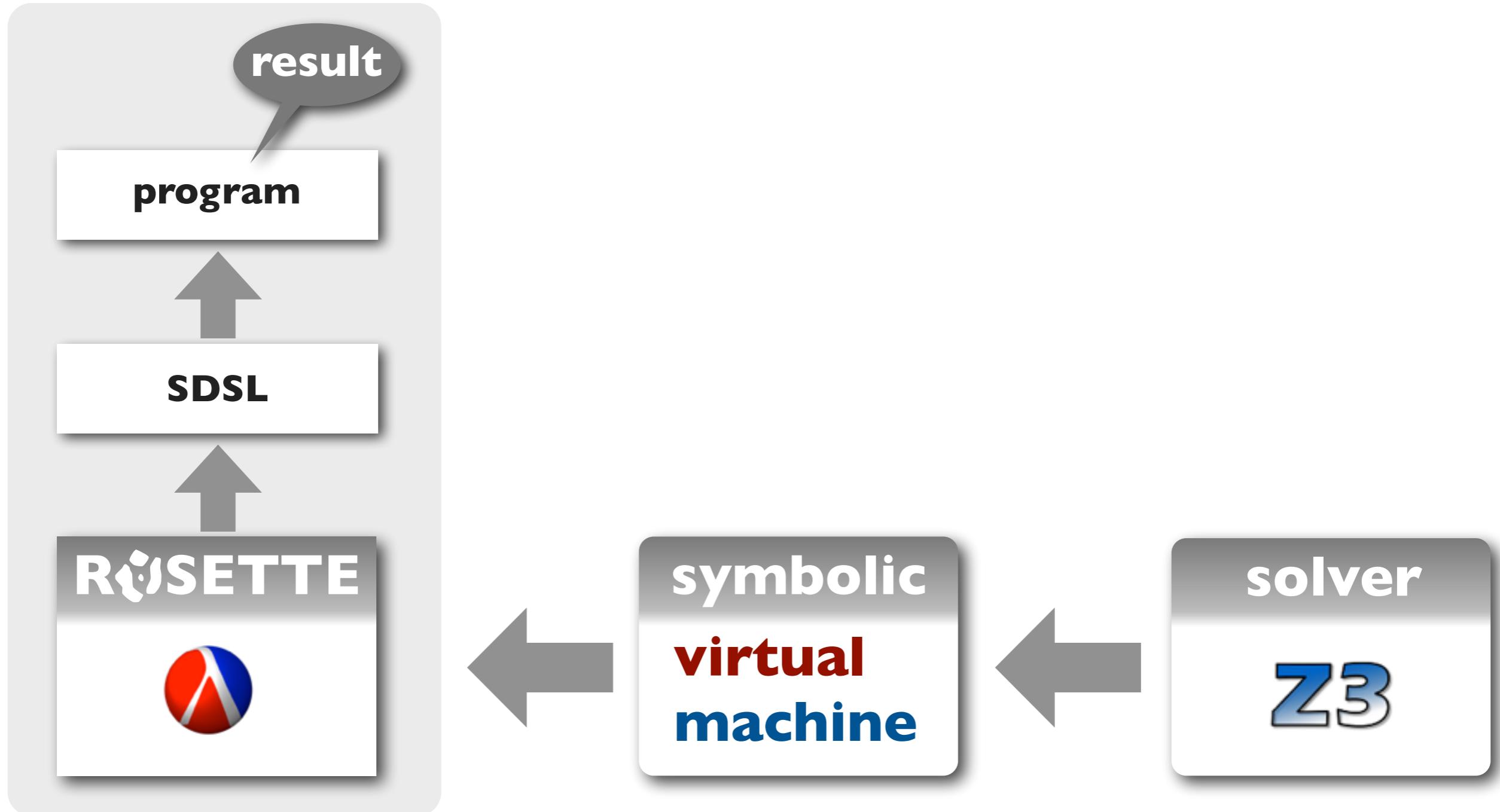
# How it all works: a big picture view



[Torlak & Bodik,  
Onward'13]

[Torlak & Bodik, PLDI'14]

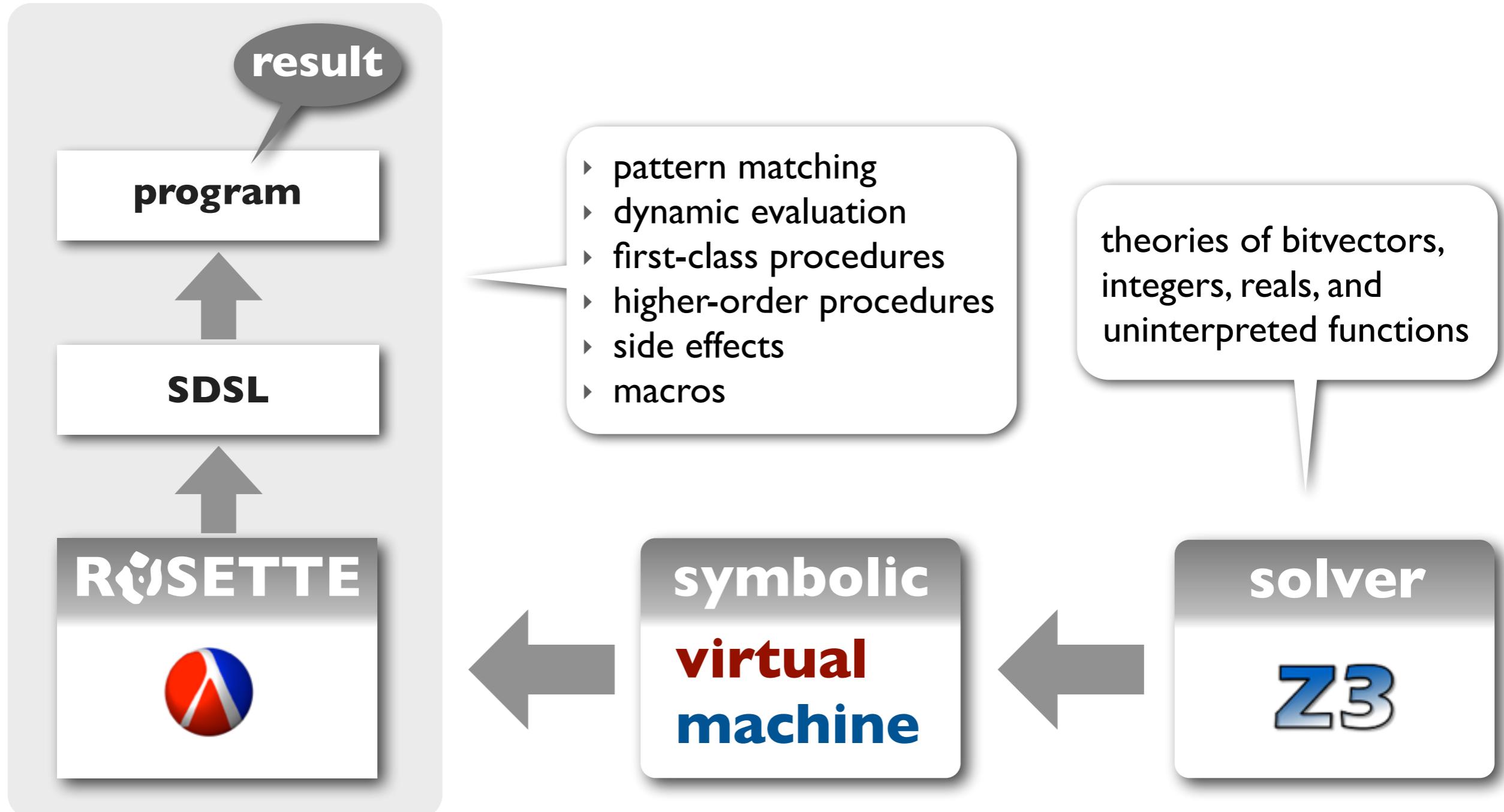
# How it all works: a big picture view



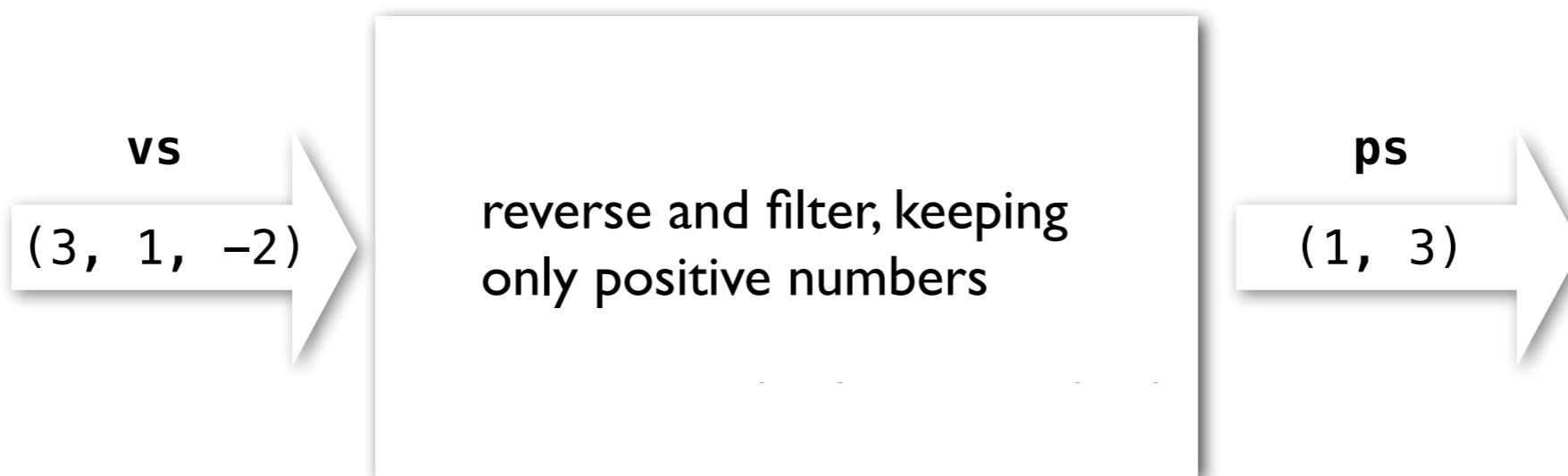
[Torlak & Bodik,  
Onward'13]

[Torlak & Bodik, PLDI'14]

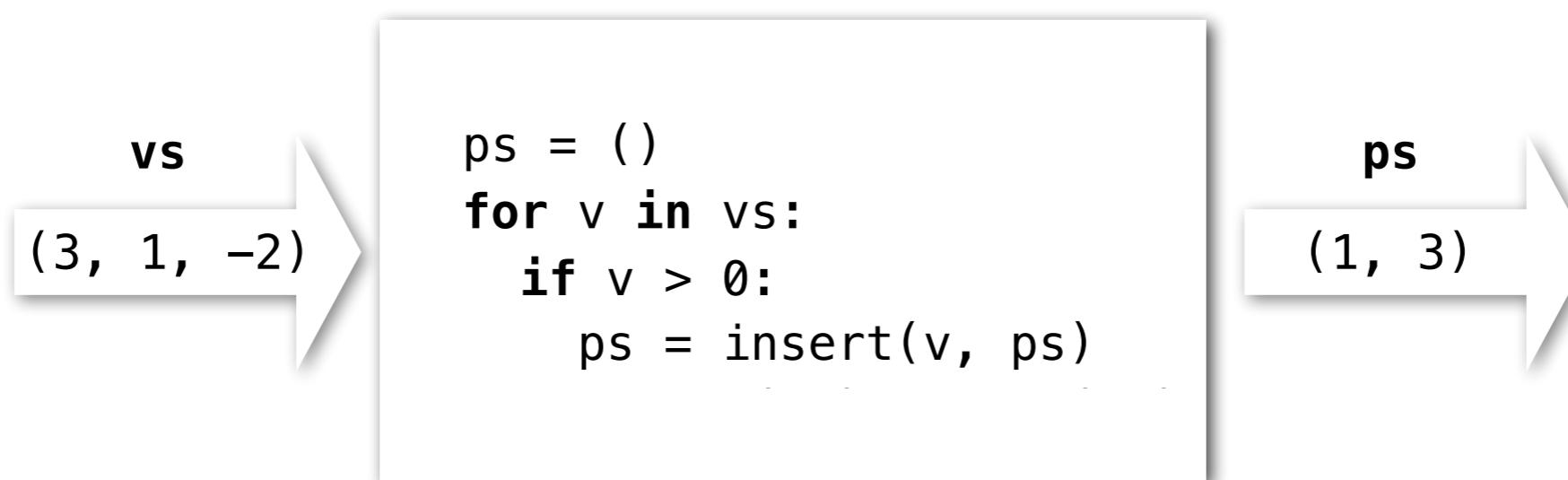
# How it all works: a big picture view



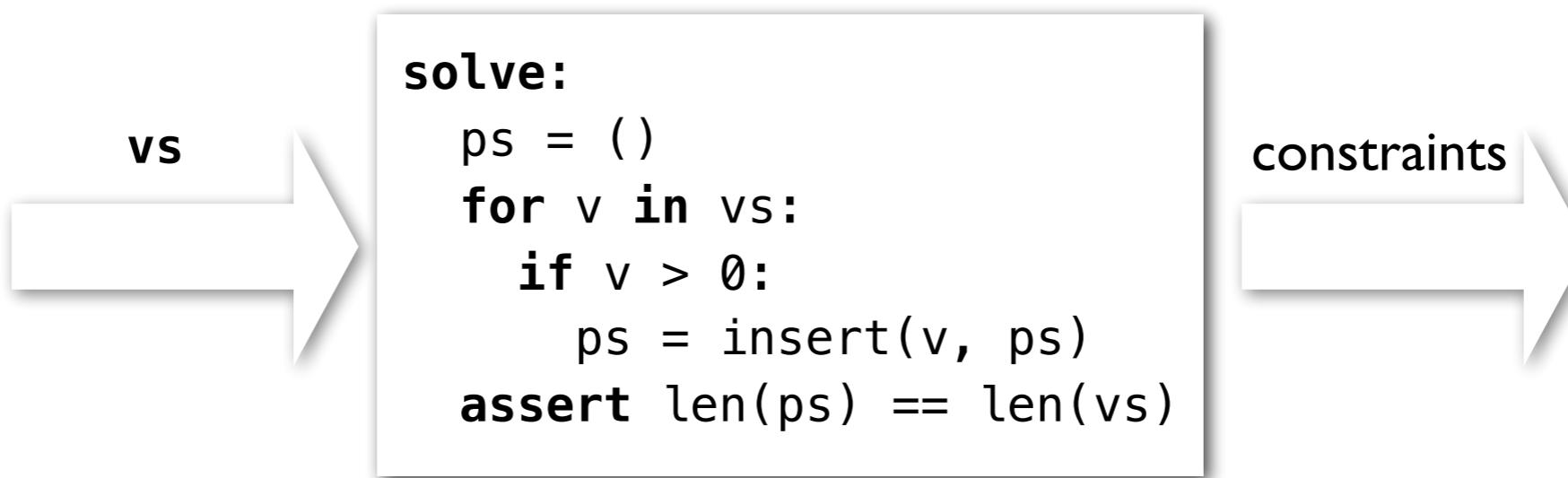
# Translation to constraints by example



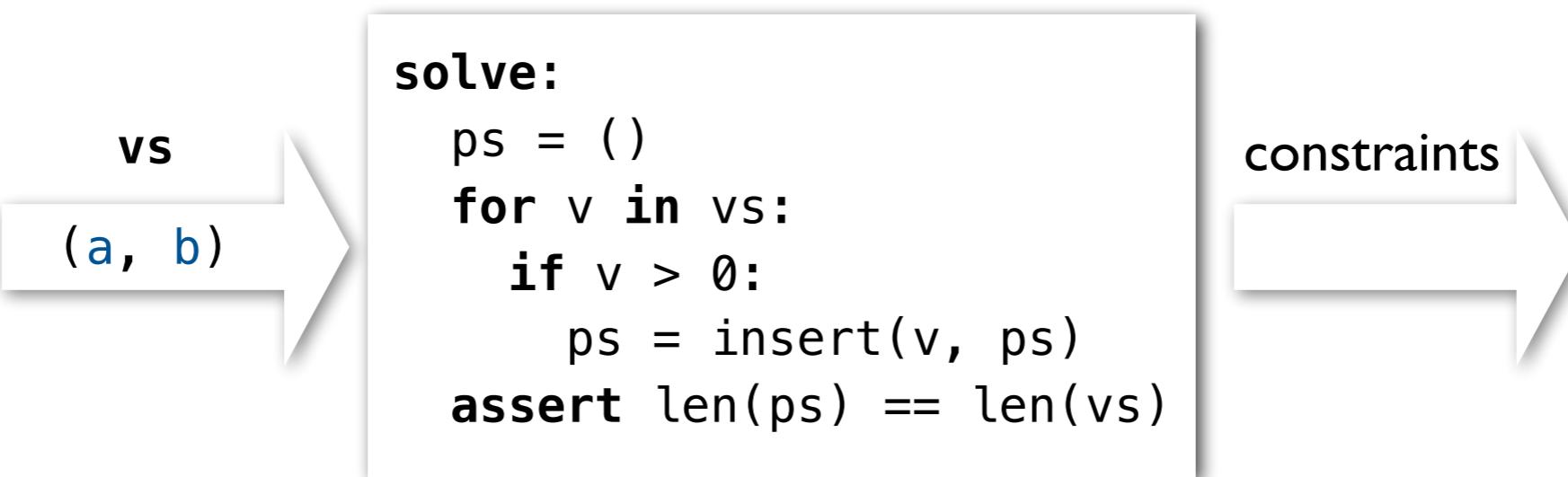
# Translation to constraints by example



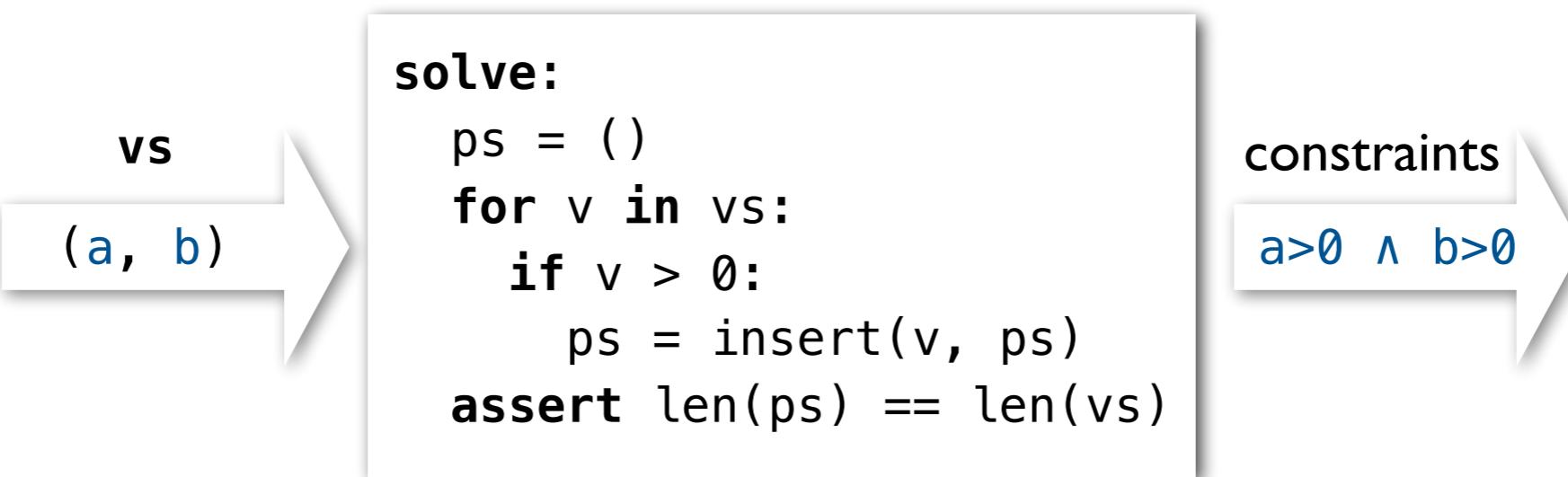
# Translation to constraints by example



# Translation to constraints by example



# Translation to constraints by example

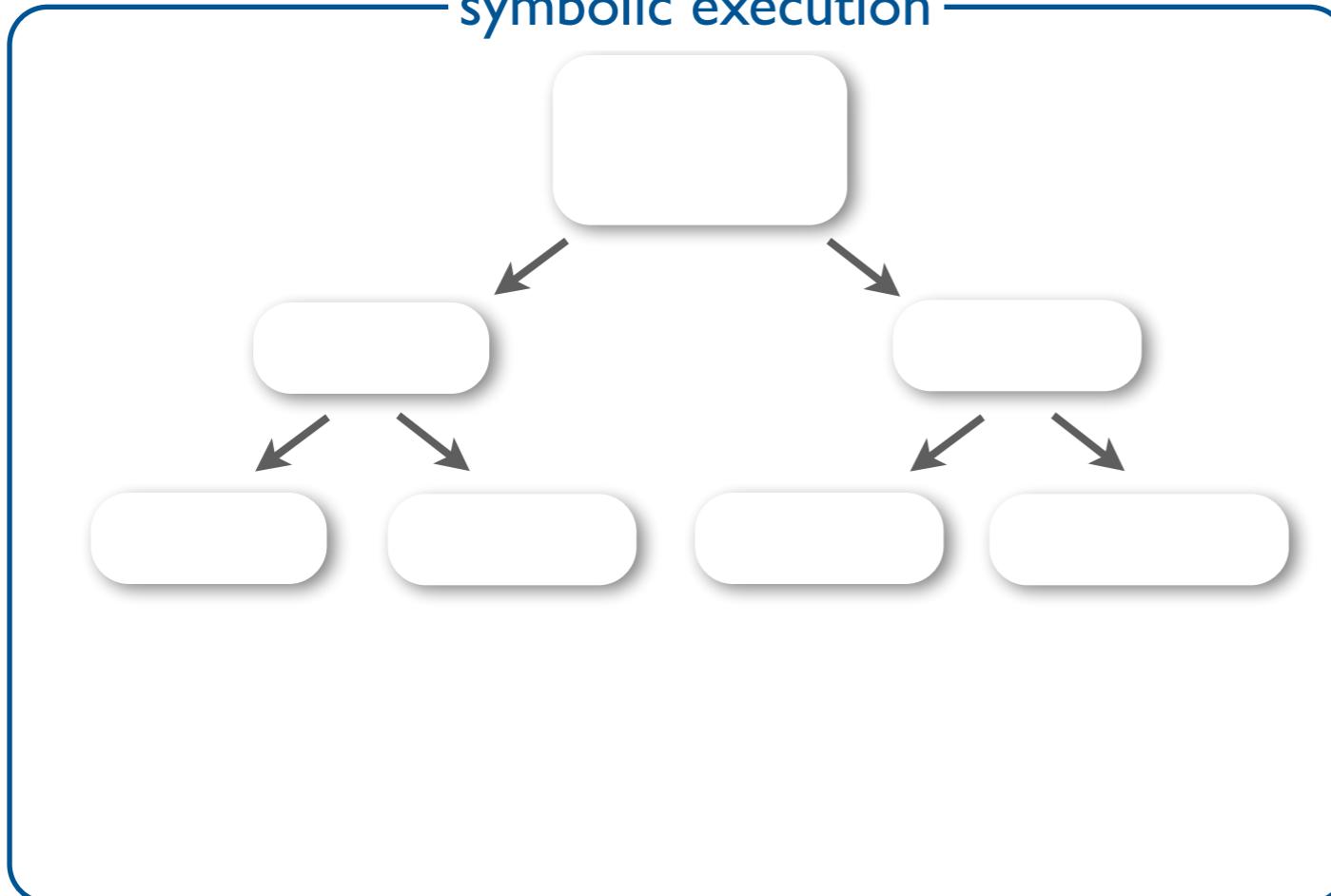


# Design space of precise symbolic encodings

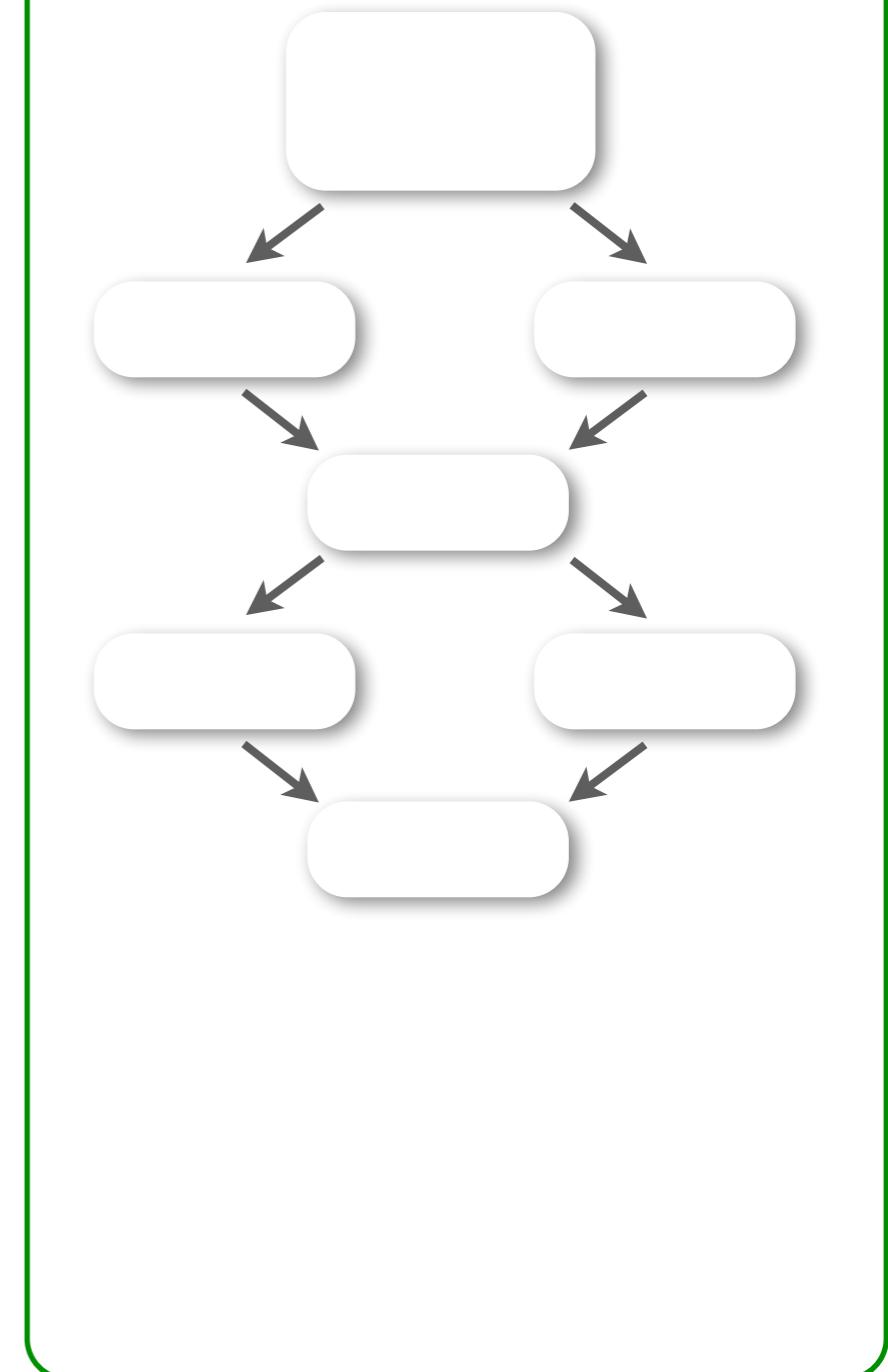
**solve:**

```
ps = ()  
for v in vs:  
    if v > 0:  
        ps = insert(v, ps)  
assert len(ps) == len(vs)
```

symbolic execution



bounded model checking



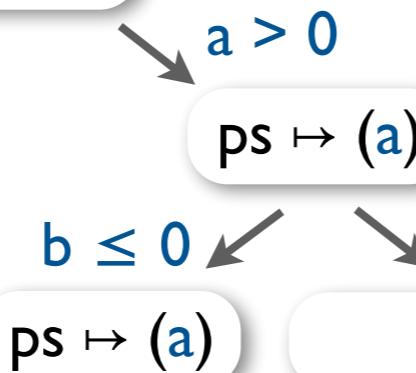
# Design space of precise symbolic encodings

**solve:**

```
ps = ()  
for v in vs:  
    if v > 0:  
        ps = insert(v, ps)  
assert len(ps) == len(vs)
```

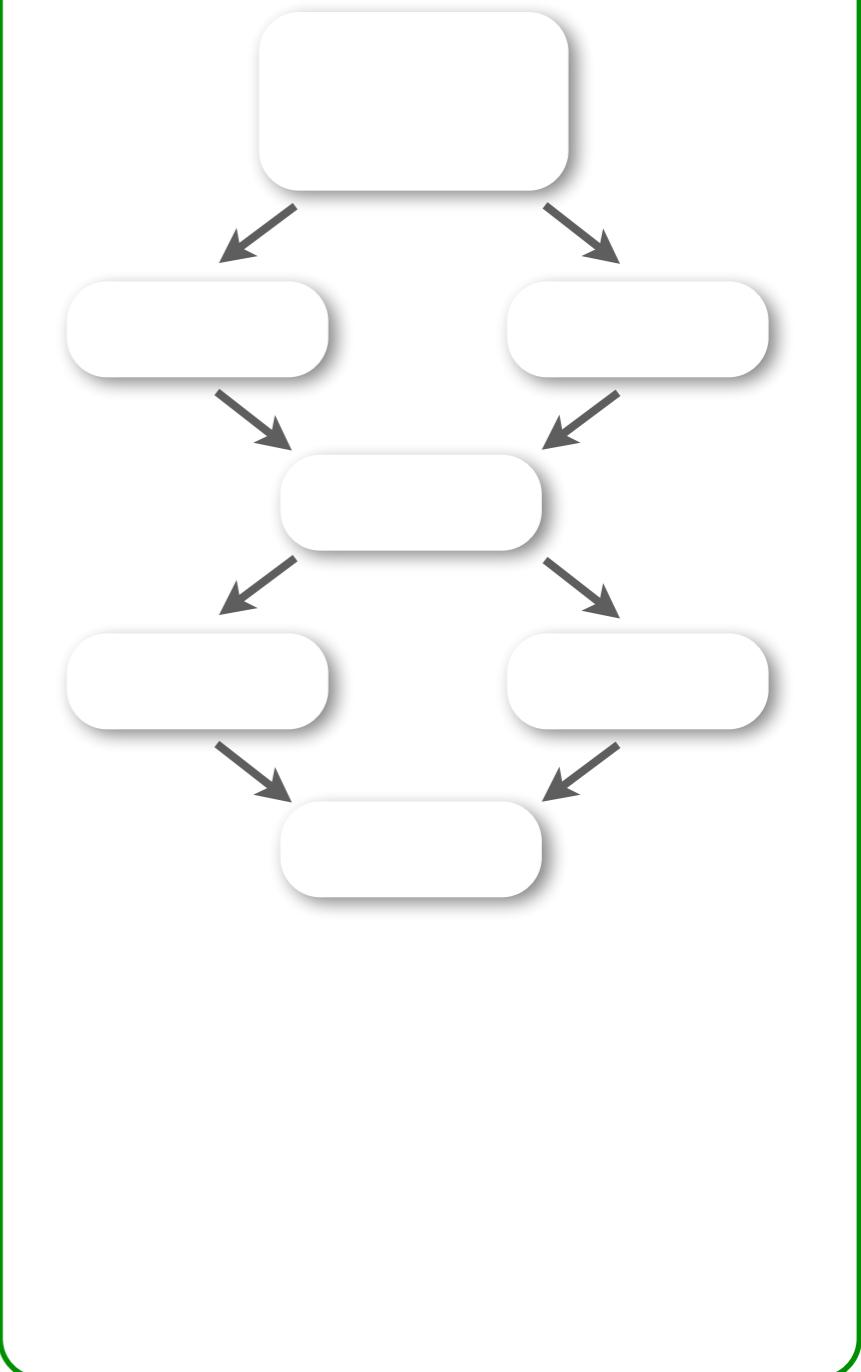
symbolic execution

$vs \mapsto (a, b)$   
 $ps \mapsto ()$



$$\left\{ \begin{array}{l} a > 0 \\ b \leq 0 \\ \text{false} \end{array} \right\}$$

bounded model checking



# Design space of precise symbolic encodings

solve:

```
ps = ()  
for v in vs:  
    if v > 0:  
        ps = insert(v, ps)  
assert len(ps) == len(vs)
```

symbolic execution

$vs \mapsto (a, b)$   
 $ps \mapsto ()$

$a \leq 0$

$ps \mapsto ()$

$a > 0$

$ps \mapsto (a)$

$b \leq 0$

$ps \mapsto ()$

$b > 0$

$ps \mapsto (b)$

$b \leq 0$

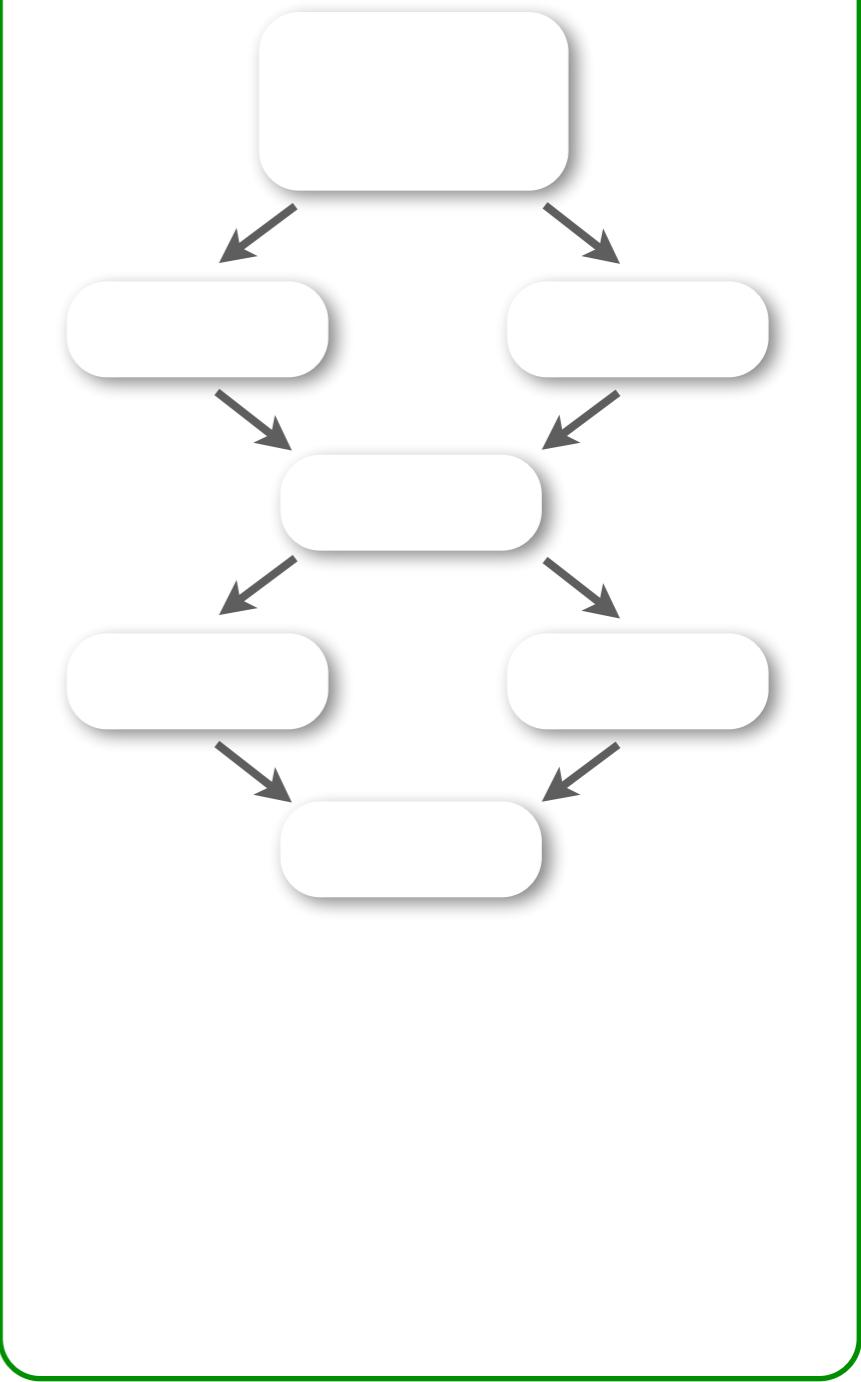
$ps \mapsto (a)$

$b > 0$

$ps \mapsto (b, a)$

$$\left\{ \begin{array}{l} a \leq 0 \\ b \leq 0 \\ \text{false} \end{array} \right\} \vee \left\{ \begin{array}{l} a \leq 0 \\ b > 0 \\ \text{false} \end{array} \right\} \vee \left\{ \begin{array}{l} a > 0 \\ b \leq 0 \\ \text{false} \end{array} \right\} \vee \left\{ \begin{array}{l} a > 0 \\ b > 0 \\ \text{true} \end{array} \right\}$$

bounded model checking

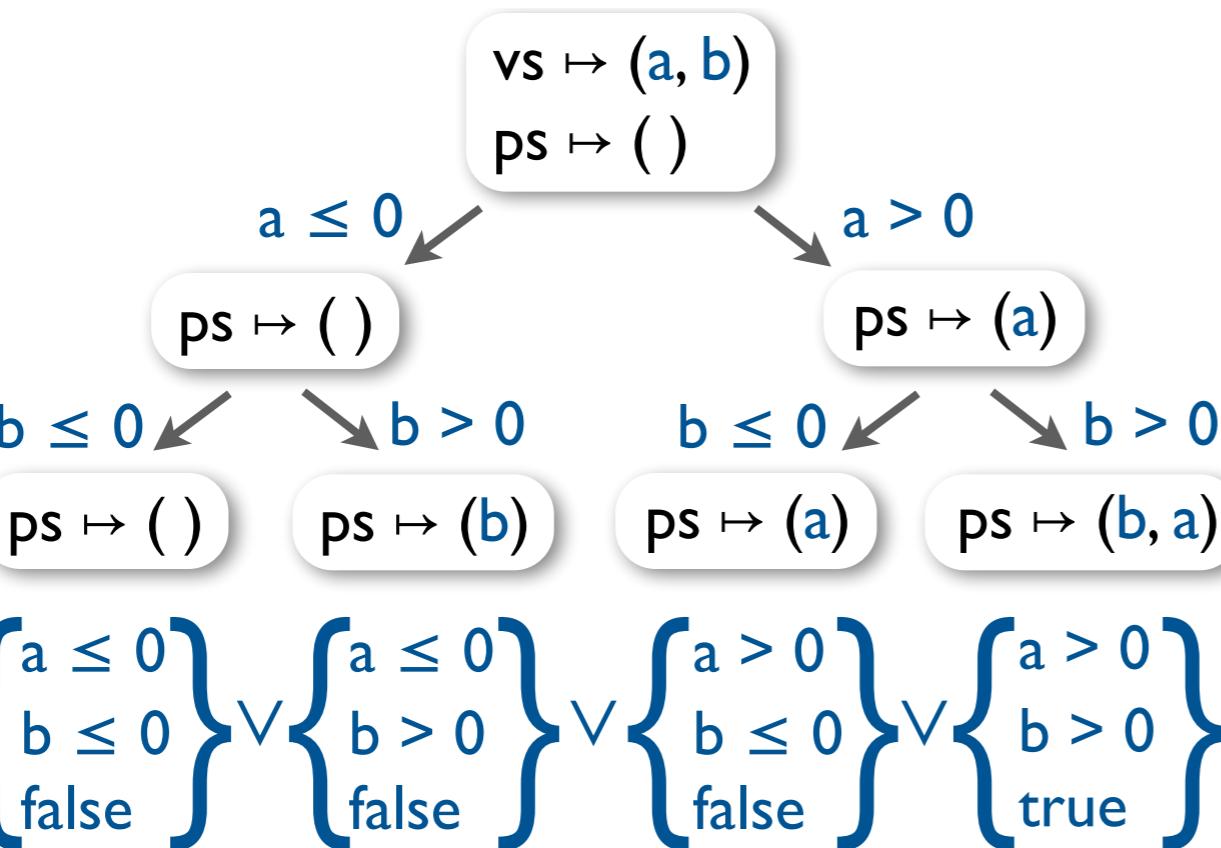


# Design space of precise symbolic encodings

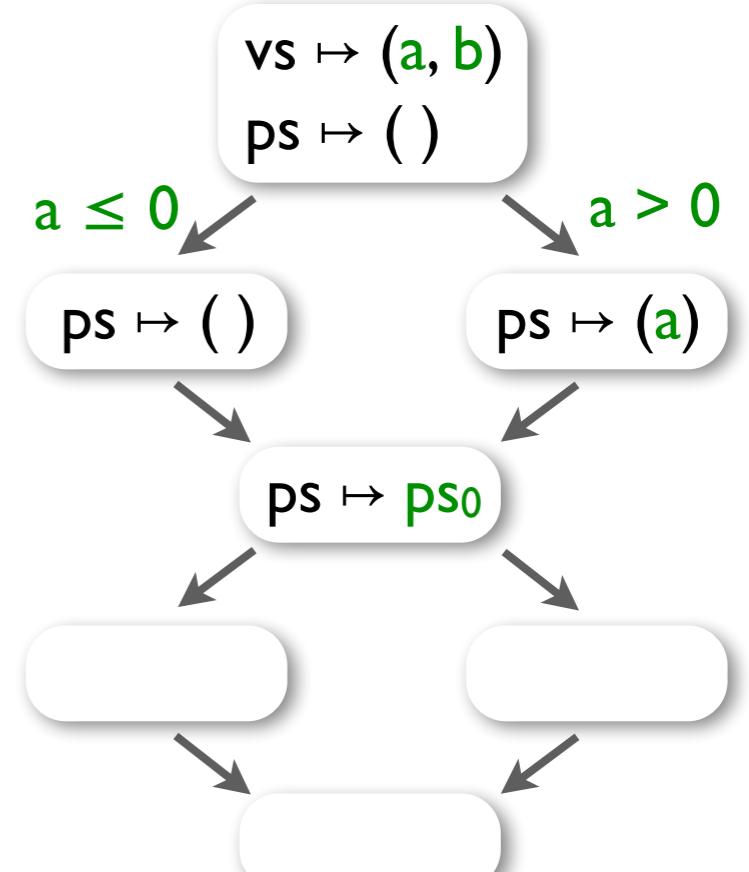
solve:

```
ps = ()  
for v in vs:  
    if v > 0:  
        ps = insert(v, ps)  
assert len(ps) == len(vs)
```

symbolic execution



bounded model checking



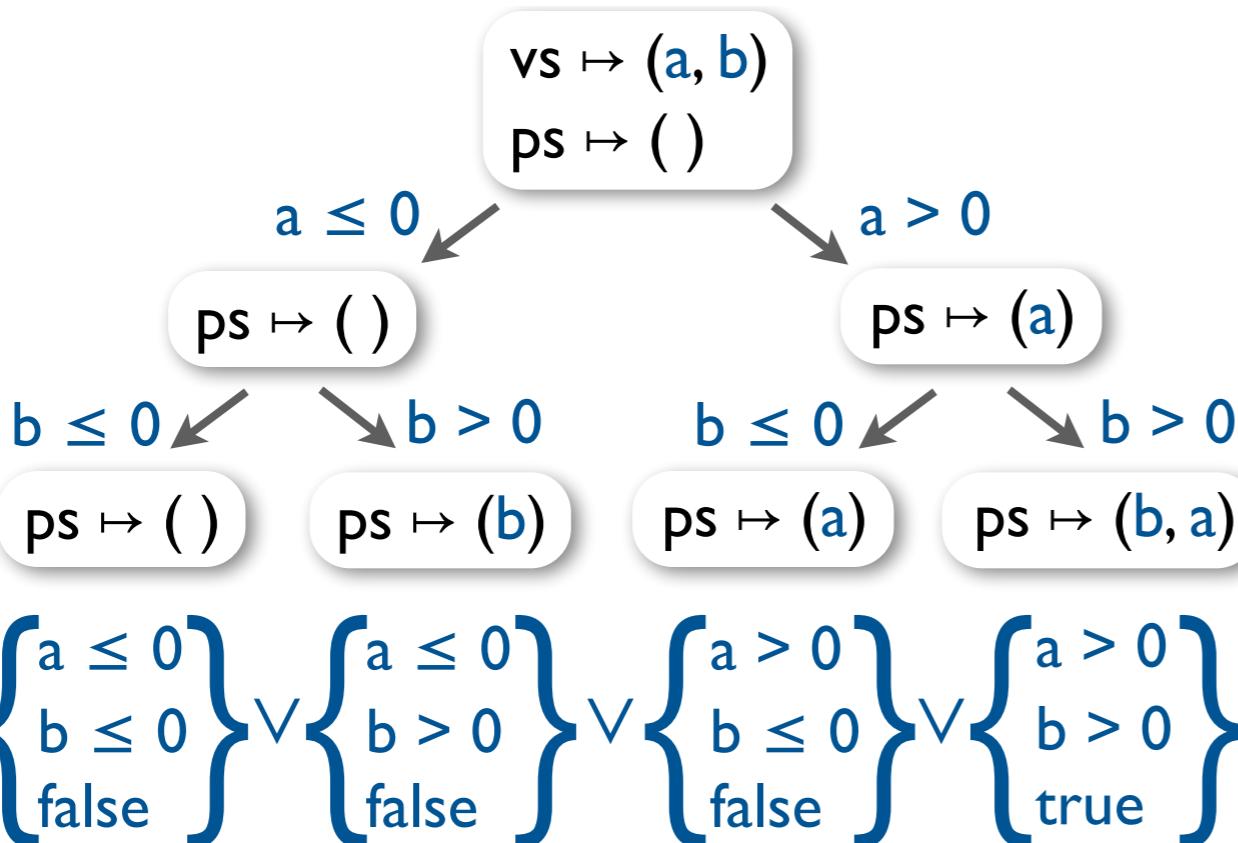
$$ps_0 = \text{ite}(a > 0, (a), ( ))$$

# Design space of precise symbolic encodings

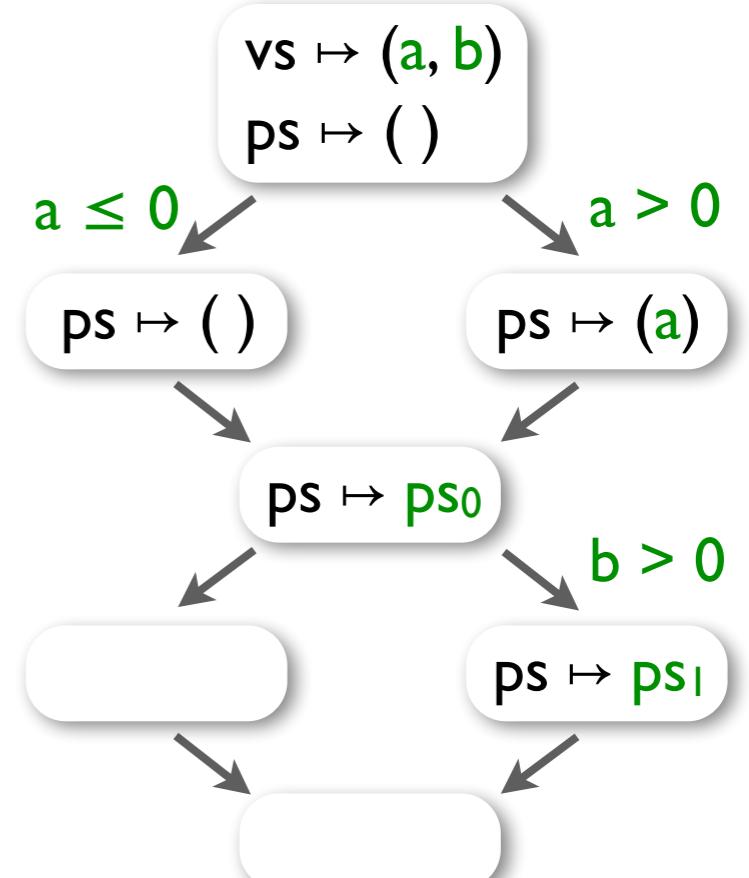
solve:

```
ps = ()  
for v in vs:  
    if v > 0:  
        ps = insert(v, ps)  
assert len(ps) == len(vs)
```

symbolic execution



bounded model checking



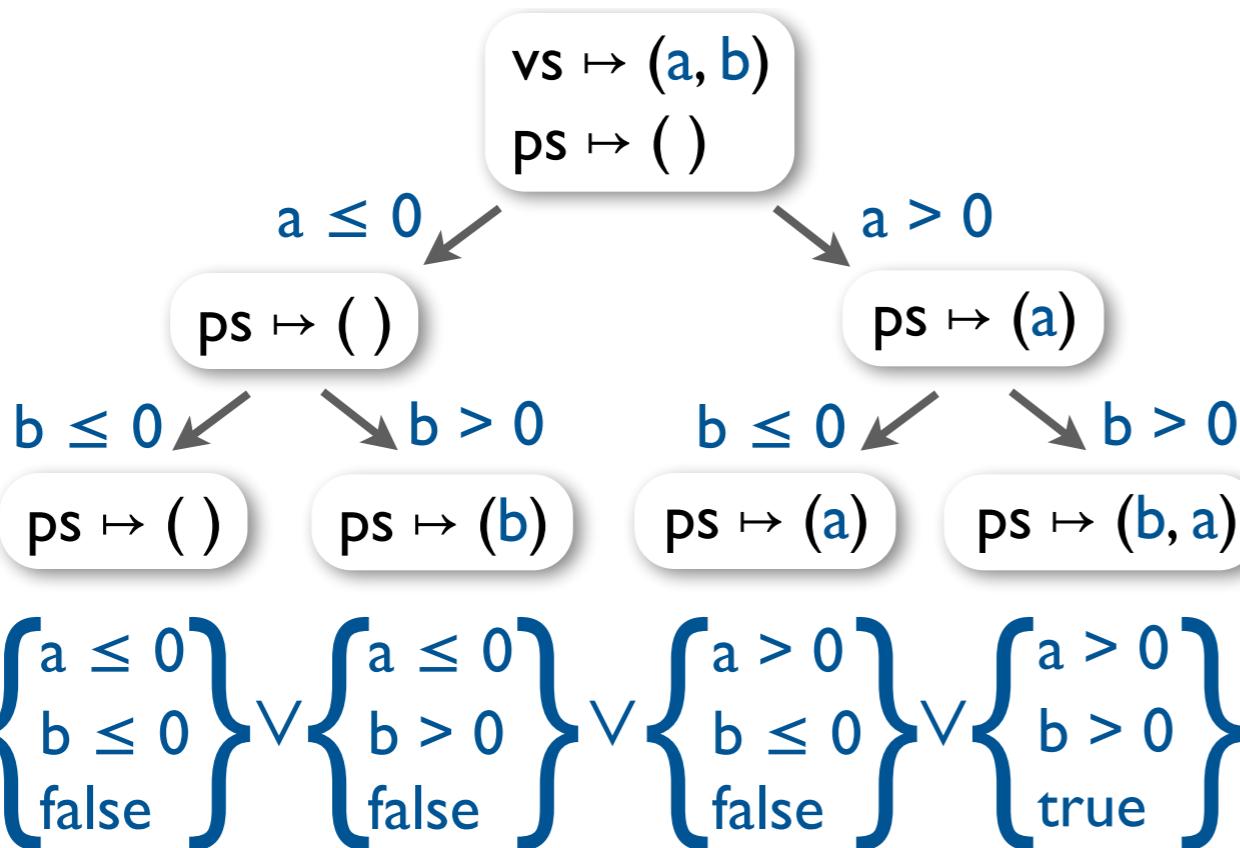
$$\begin{aligned} ps_0 &= \text{ite}(a > 0, (a), ()) \\ ps_1 &= \text{insert}(b, ps_0) \end{aligned}$$

# Design space of precise symbolic encodings

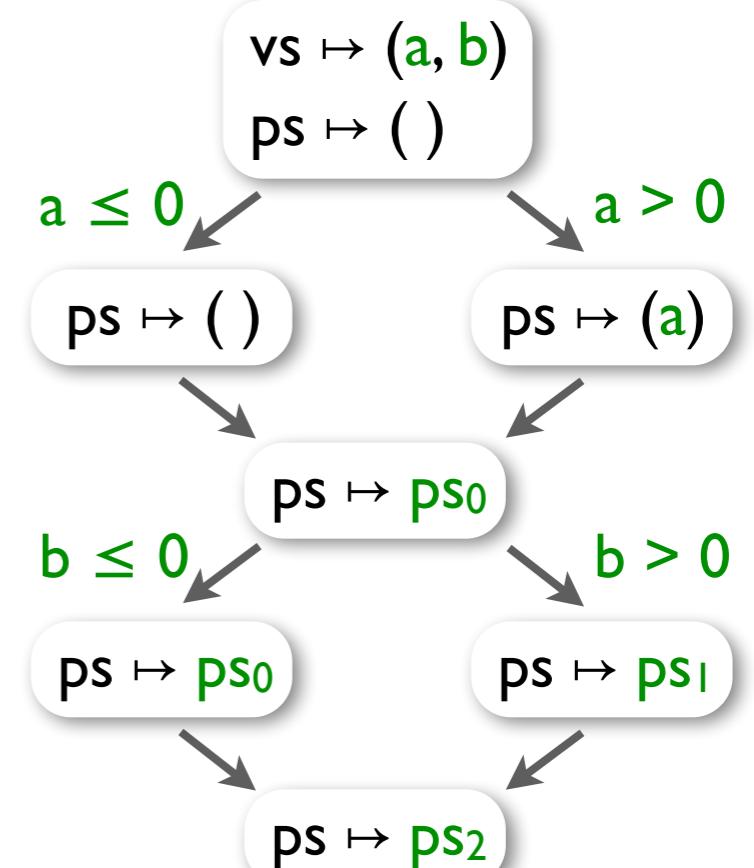
**solve:**

```
ps = ()
for v in vs:
    if v > 0:
        ps = insert(v, ps)
assert len(ps) == len(vs)
```

symbolic execution



bounded model checking

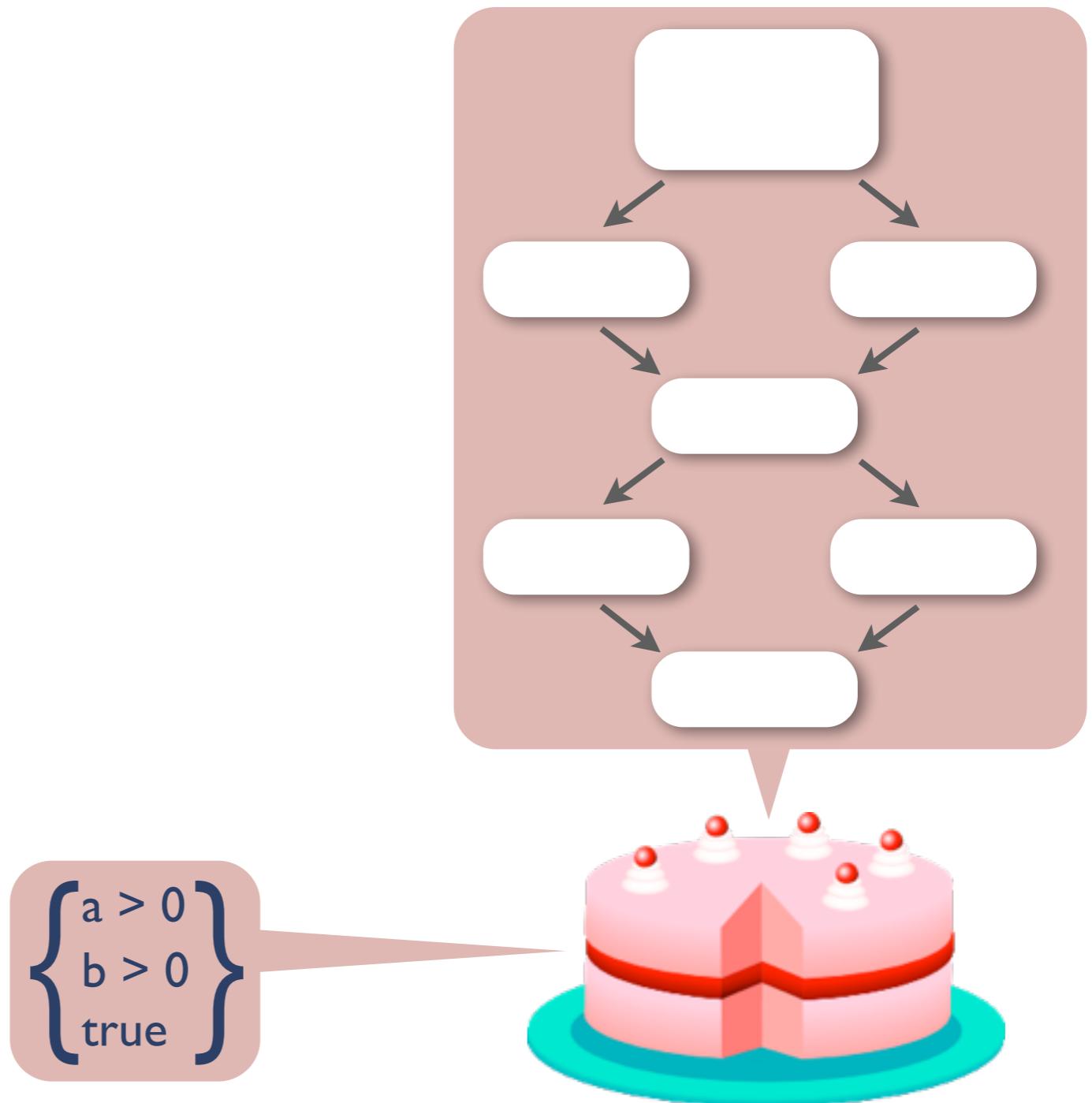


$ps_0 = \text{ite}(a > 0, (a), ( ))$   
 $ps_1 = \text{insert}(b, ps_0)$   
 $ps_2 = \text{ite}(b > 0, ps_0, ps_1)$   
 $\text{assert } \text{len}(ps_2) = 2$

# A new design: type-driven state merging

**solve:**

```
ps = ()  
for v in vs:  
    if v > 0:  
        ps = insert(v, ps)  
assert len(ps) == len(vs)
```



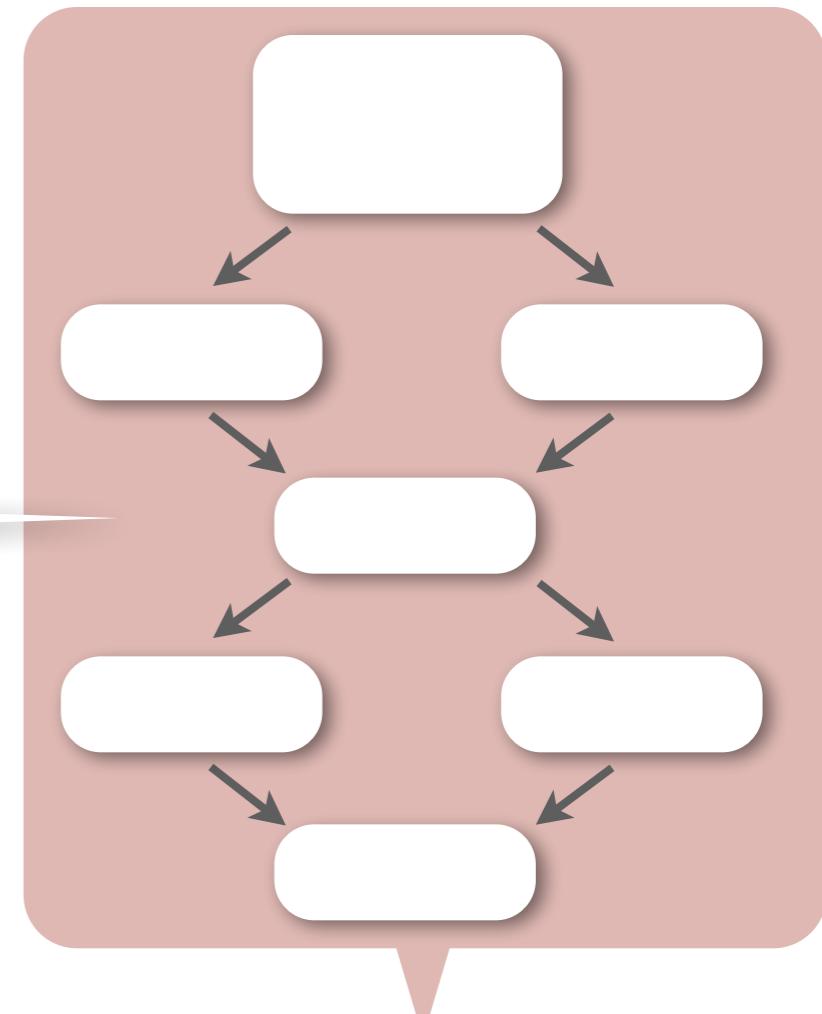
# A new design: type-driven state merging

**solve:**

```
ps = ()  
for v in vs:  
    if v > 0:  
        ps = insert(v, ps)  
assert len(ps) == len(vs)
```

**Merge values of**

- primitive types: symbolically
- immutable types: structurally
- all other types: via unions



$$\left\{ \begin{array}{l} a > 0 \\ b > 0 \\ \text{true} \end{array} \right\}$$



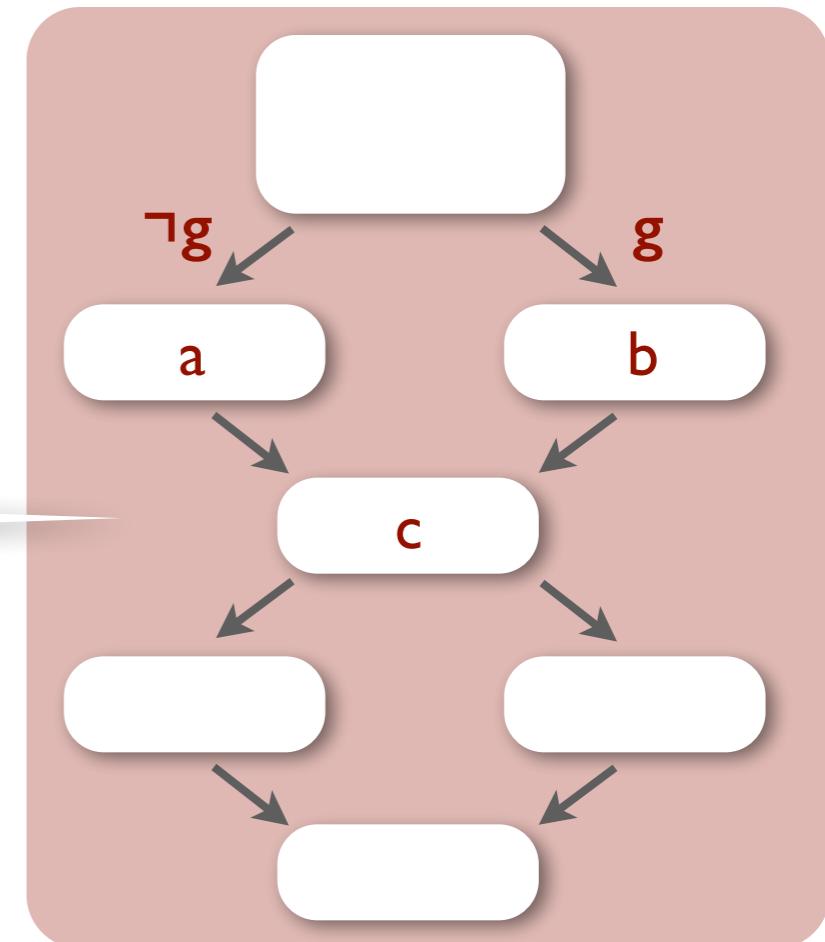
# A new design: type-driven state merging

**solve:**

```
ps = ()  
for v in vs:  
    if v > 0:  
        ps = insert(v, ps)  
assert len(ps) == len(vs)
```

**Merge values of**

- primitive types: symbolically
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$$\left\{ \begin{array}{l} a > 0 \\ b > 0 \\ \text{true} \end{array} \right\}$$



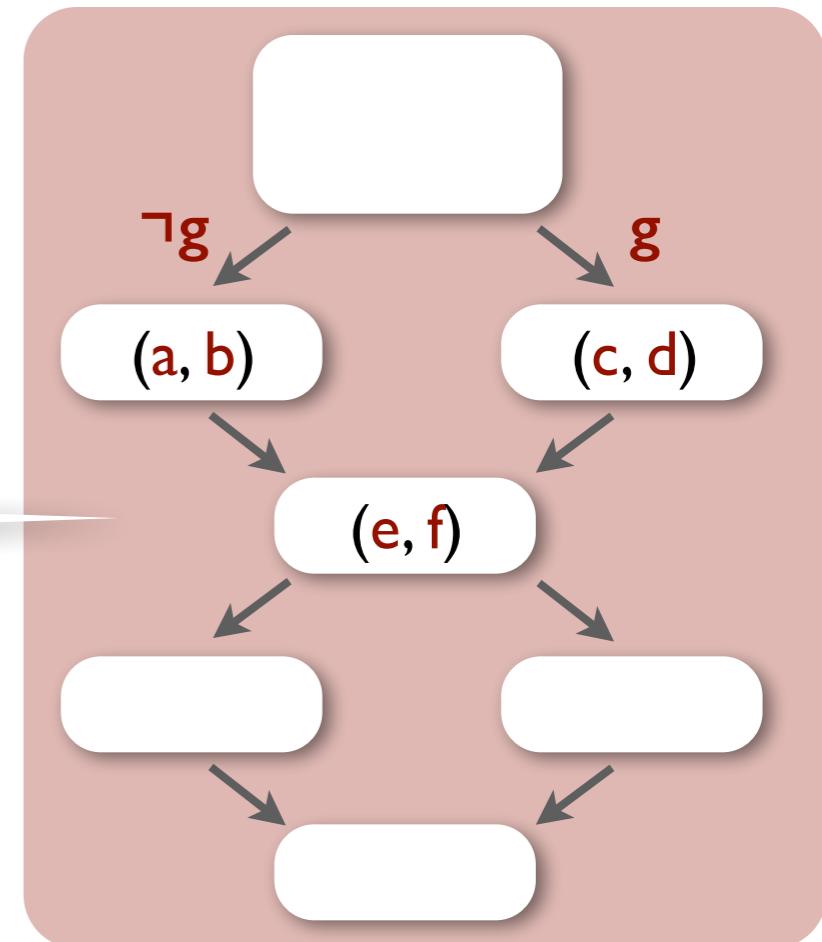
# A new design: type-driven state merging

**solve:**

```
ps = ()  
for v in vs:  
    if v > 0:  
        ps = insert(v, ps)  
assert len(ps) == len(vs)
```

**Merge values of**

- ▶ primitive types: symbolically
- ▶ immutable types: structurally
- ▶ all other types: via unions



$$\left\{ \begin{array}{l} a > 0 \\ b > 0 \\ \text{true} \end{array} \right\}$$



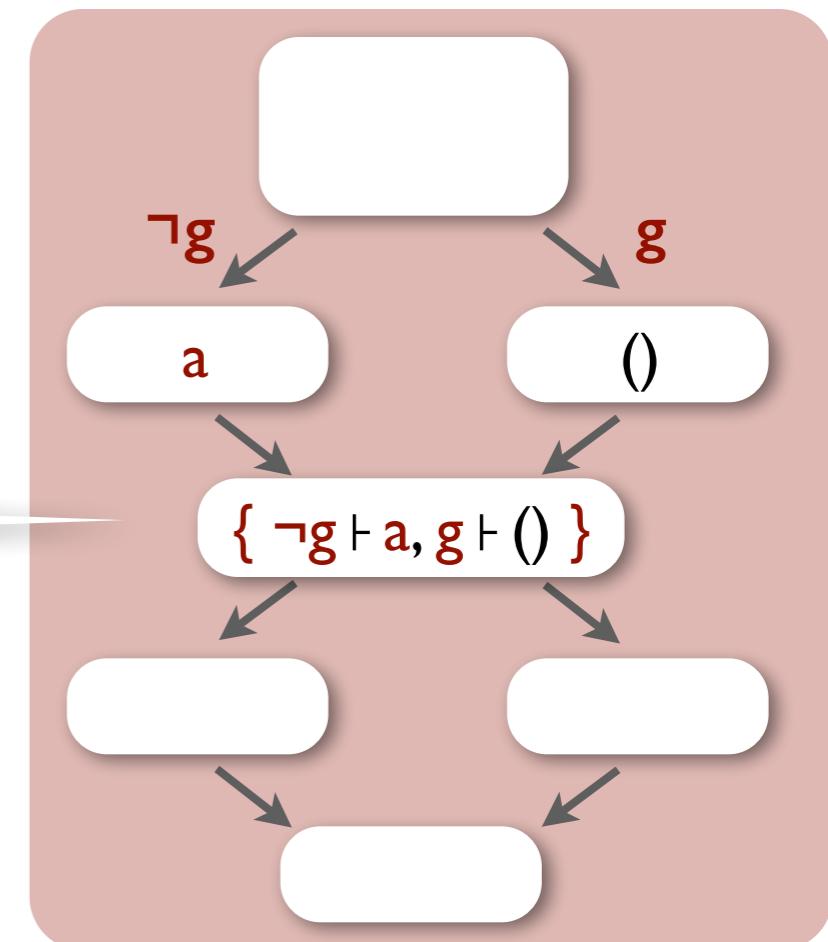
# A new design: type-driven state merging

**solve:**

```
ps = ()  
for v in vs:  
    if v > 0:  
        ps = insert(v, ps)  
assert len(ps) == len(vs)
```

**Merge values of**

- ▶ primitive types: symbolically
- ▶ immutable types: structurally
- ▶ all other types: via unions



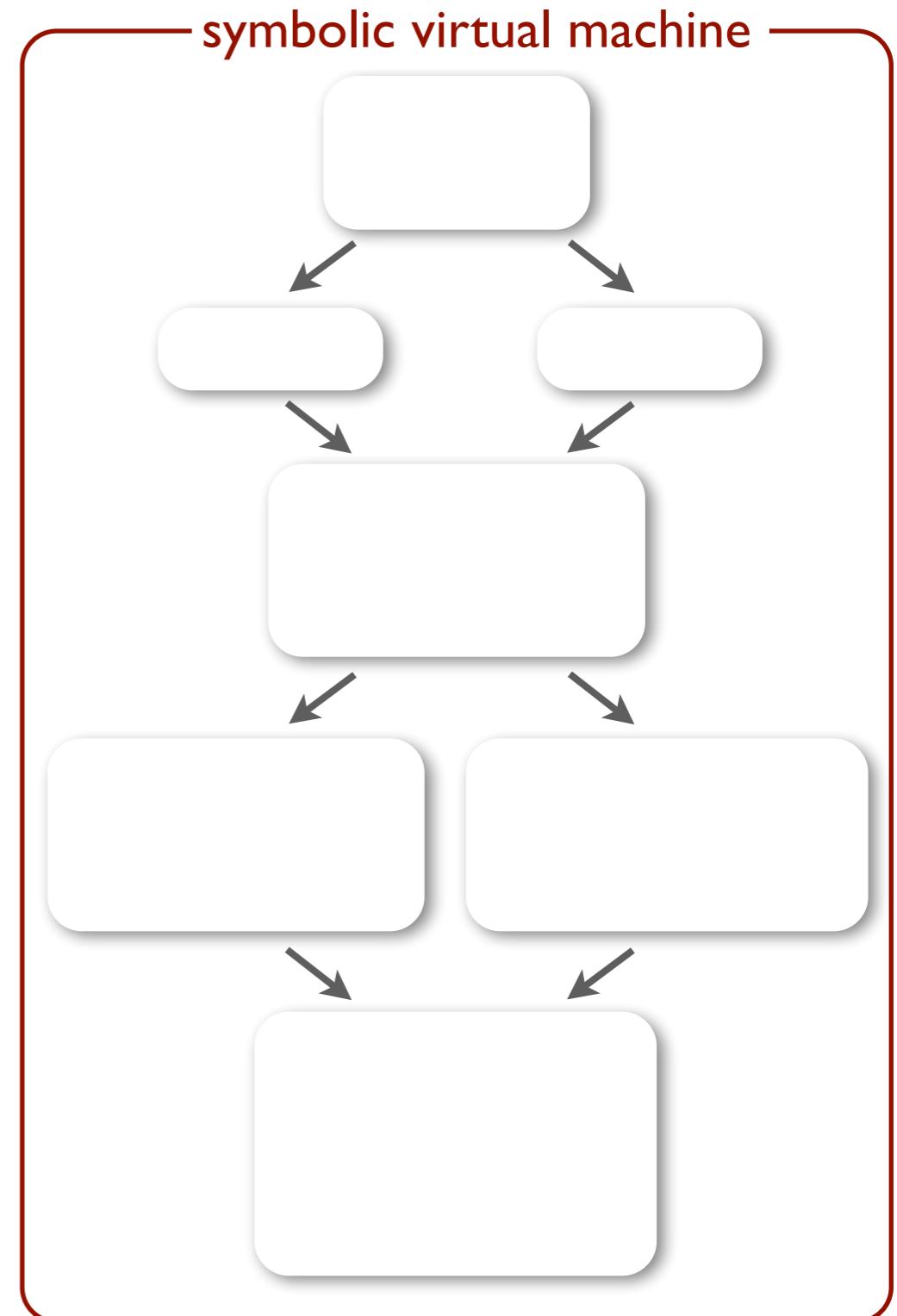
$$\left\{ \begin{array}{l} a > 0 \\ b > 0 \\ \text{true} \end{array} \right\}$$



# A new design: type-driven state merging

**solve:**

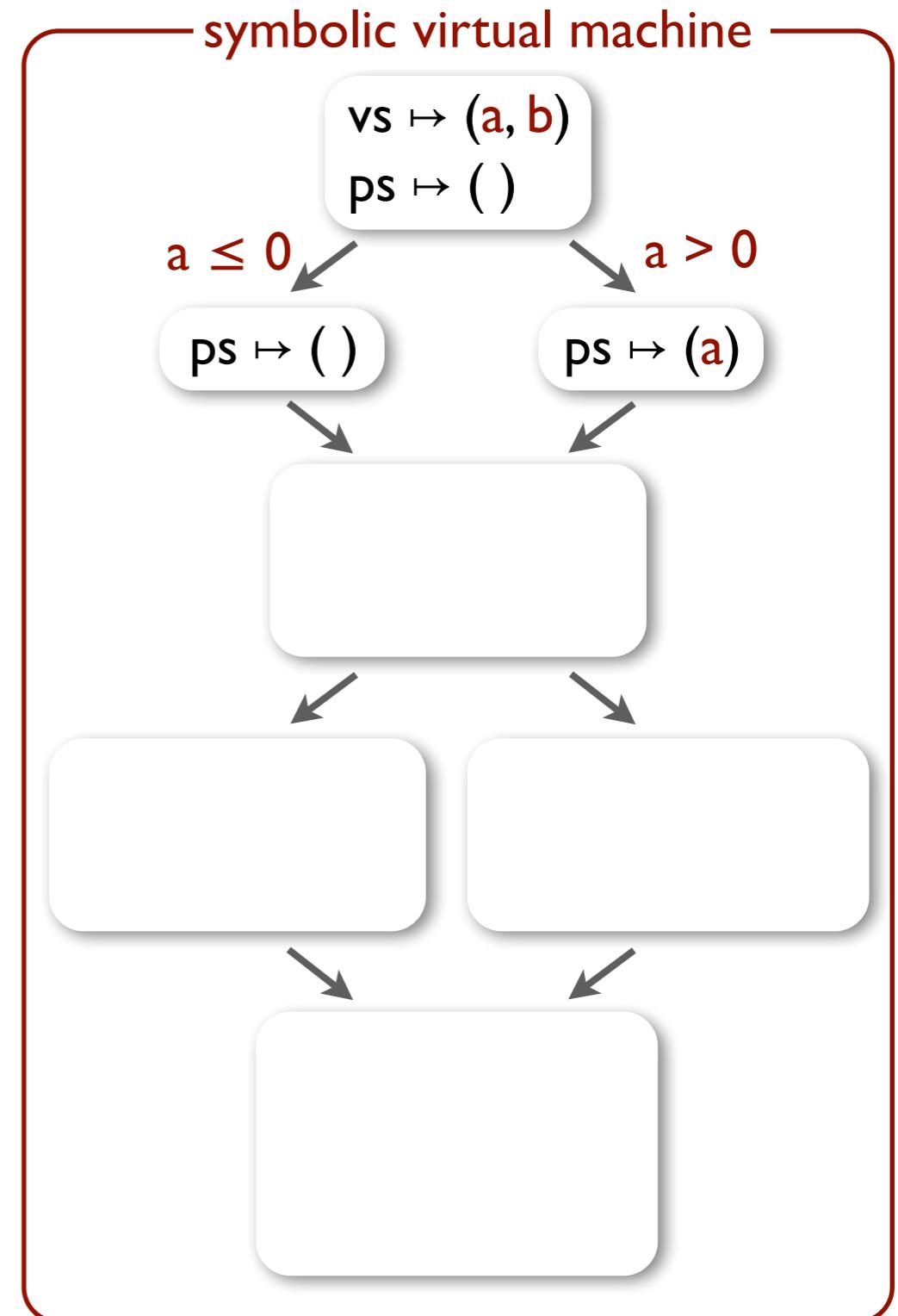
```
ps = ()  
for v in vs:  
    if v > 0:  
        ps = insert(v, ps)  
assert len(ps) == len(vs)
```



# A new design: type-driven state merging

**solve:**

```
ps = ()  
for v in vs:  
    if v > 0:  
        ps = insert(v, ps)  
assert len(ps) == len(vs)
```



# A new design: type-driven state merging

**solve:**

```
ps = ()  
for v in vs:  
    if v > 0:  
        ps = insert(v, ps)  
assert len(ps) == len(vs)
```

Symbolic union: a set of guarded values, with disjoint guards.

$g_0 = a > 0$

symbolic virtual machine

$vs \mapsto (a, b)$   
 $ps \mapsto ()$

$\neg g_0$   $g_0$   
 $ps \mapsto ()$   $ps \mapsto (a)$

$ps \mapsto \{ g_0 \vdash (a),$   
 $\neg g_0 \vdash () \}$

# A new design: type-driven state merging

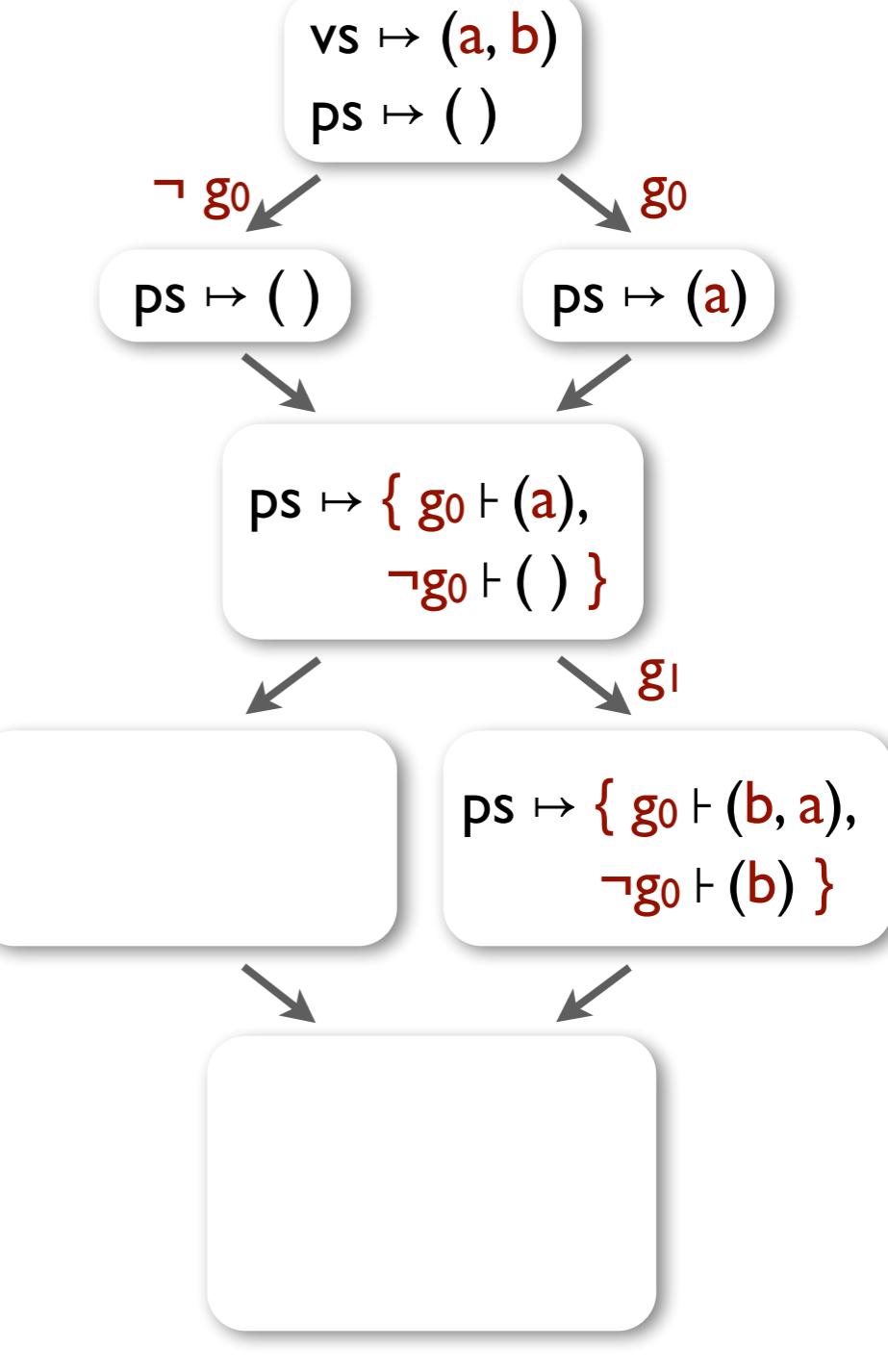
**solve:**

```
ps = ()  
for v in vs:  
    if v > 0:  
        ps = insert(v, ps)  
assert len(ps) == len(vs)
```

Execute insert  
concretely on all  
lists in the union.

$$\begin{aligned}g_0 &= a > 0 \\g_1 &= b > 0\end{aligned}$$

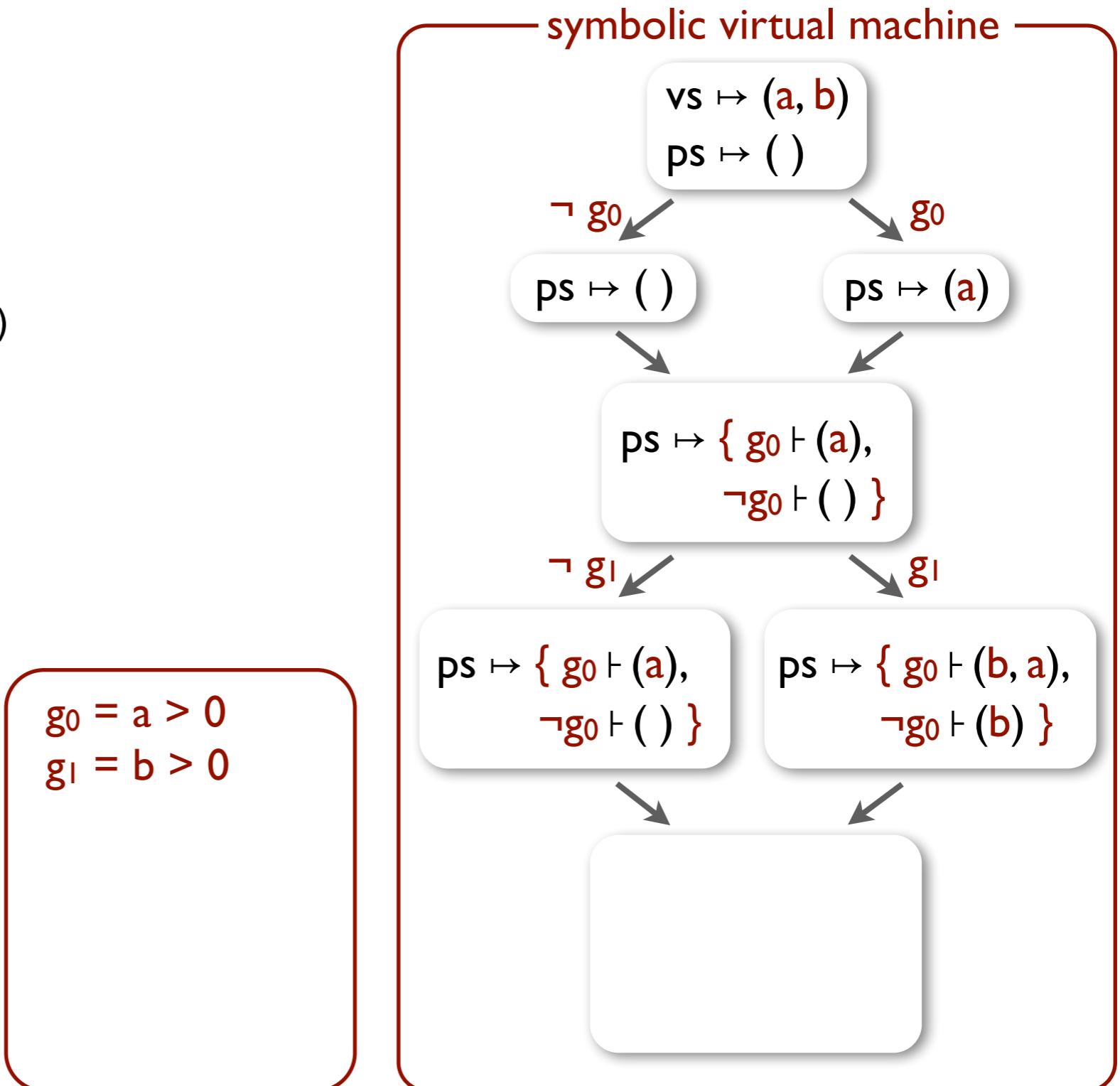
symbolic virtual machine



# A new design: type-driven state merging

**solve:**

```
ps = ()  
for v in vs:  
    if v > 0:  
        ps = insert(v, ps)  
assert len(ps) == len(vs)
```



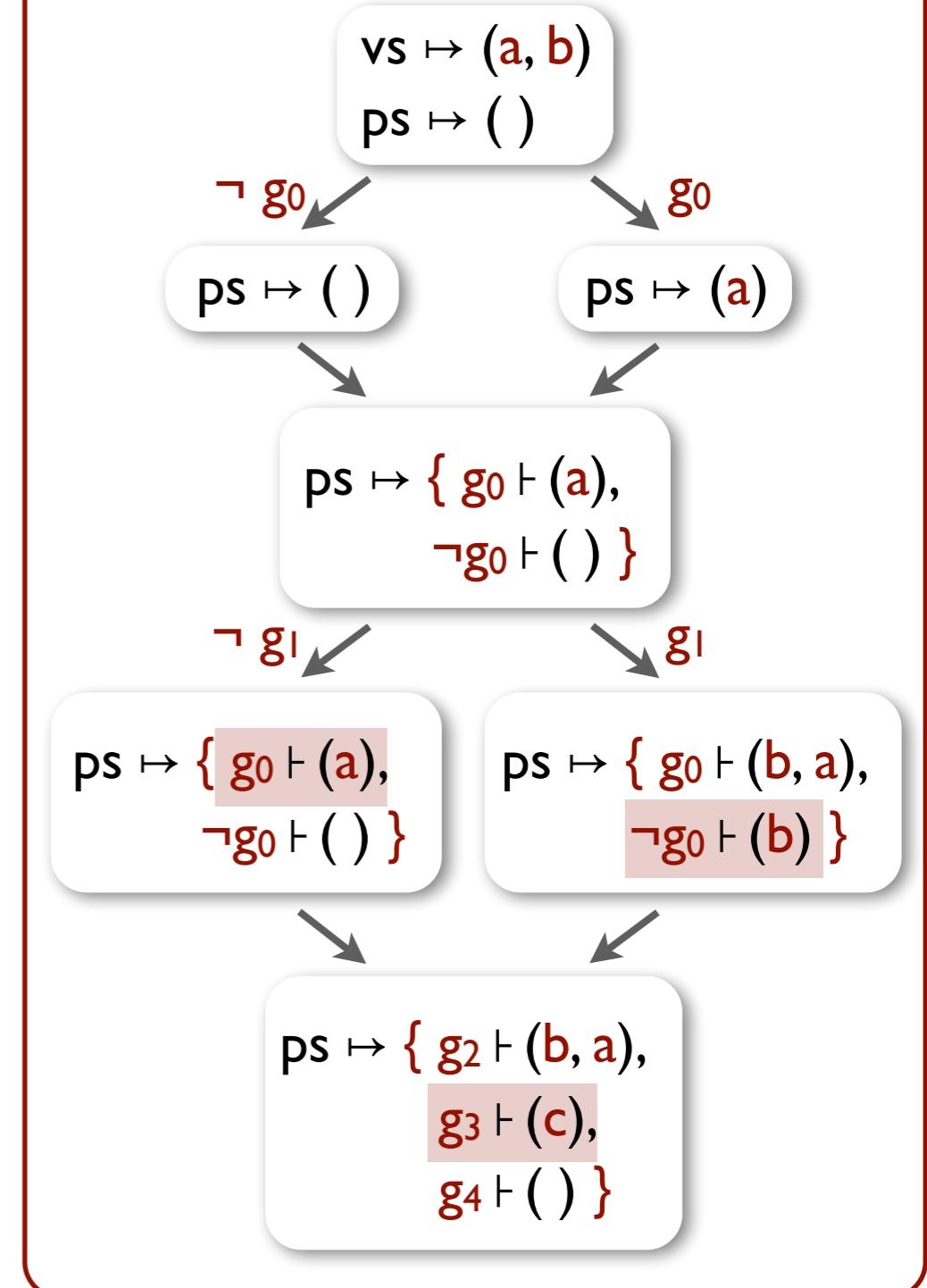
# A new design: type-driven state merging

**solve:**

```
ps = ()  
for v in vs:  
    if v > 0:  
        ps = insert(v, ps)  
assert len(ps) == len(vs)
```

$g_0 = a > 0$   
 $g_1 = b > 0$   
 $g_2 = g_0 \wedge g_1$   
 $g_3 = \neg(g_0 \Leftrightarrow g_1)$   
 $g_4 = \neg g_0 \wedge \neg g_1$   
 $c = \text{ite}(g_1, b, a)$

symbolic virtual machine



# A new design: type-driven state merging

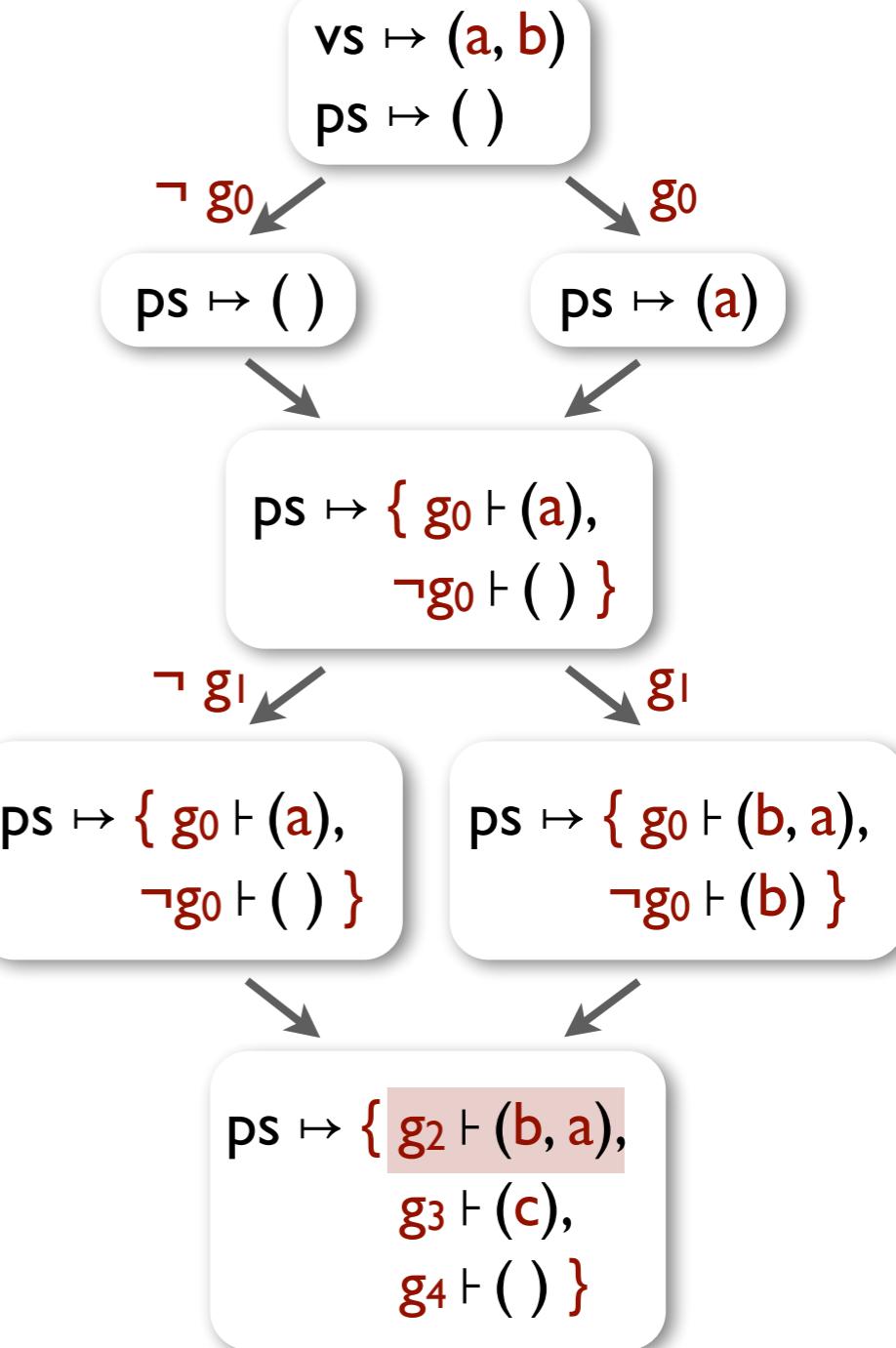
solve:

```
ps = ()  
for v in vs:  
    if v > 0:  
        ps = insert(v, ps)  
assert len(ps) == len(vs)
```

Evaluate `len` concretely  
on all lists in the union;  
assertion true only on  
the list guarded by  $g_2$ .

```
g0 = a > 0  
g1 = b > 0  
g2 = g0 ∧ g1  
g3 = ¬(g0 ⇔ g1)  
g4 = ¬g0 ∧ ¬g1  
c = ite(g1, b, a)  
assert g2
```

symbolic virtual machine



# A new design: type-driven state merging

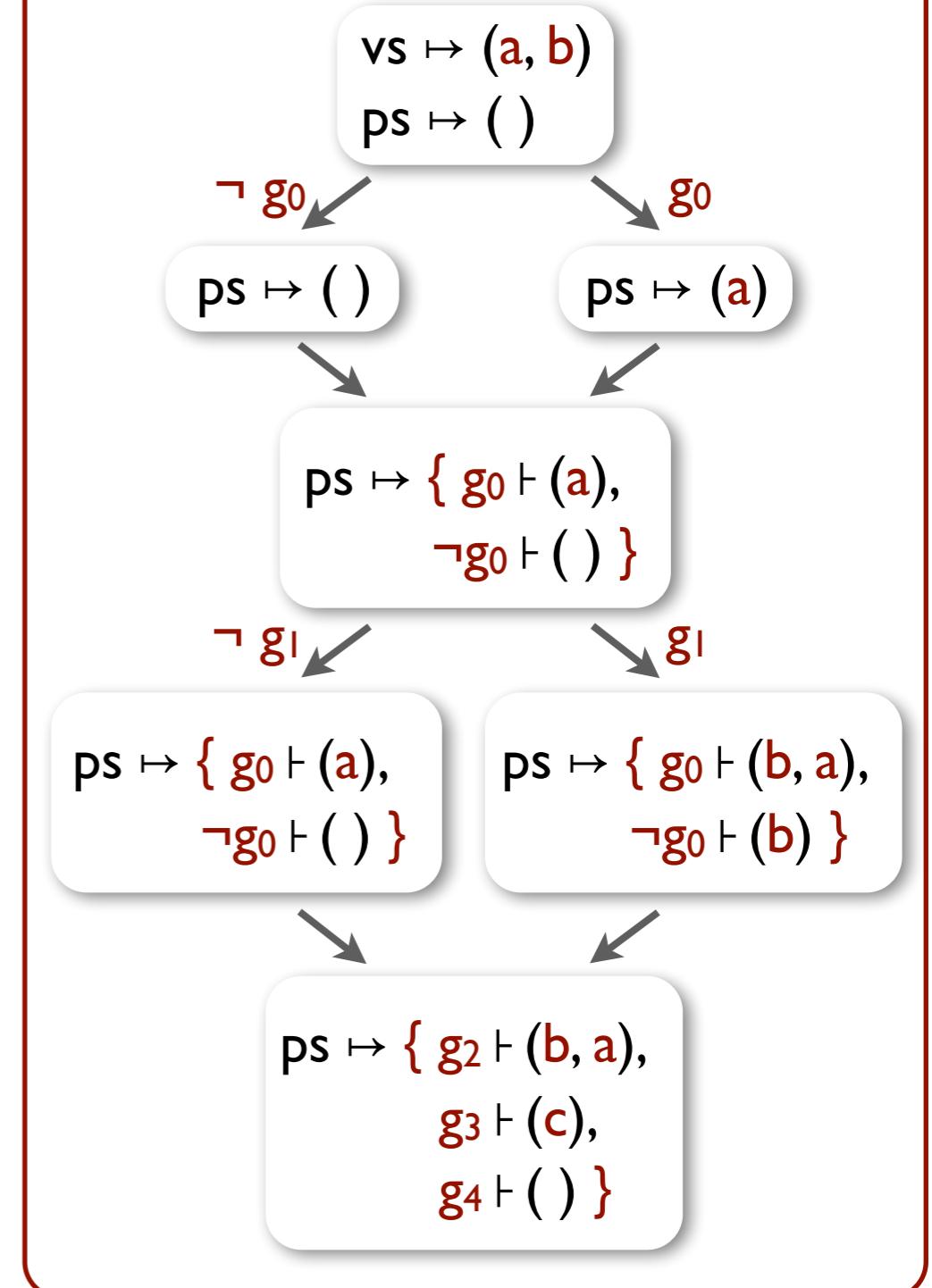
solve:

```
ps = ()  
for v in vs:  
    if v > 0:  
        ps = insert(v, ps)  
assert len(ps) == len(vs)
```

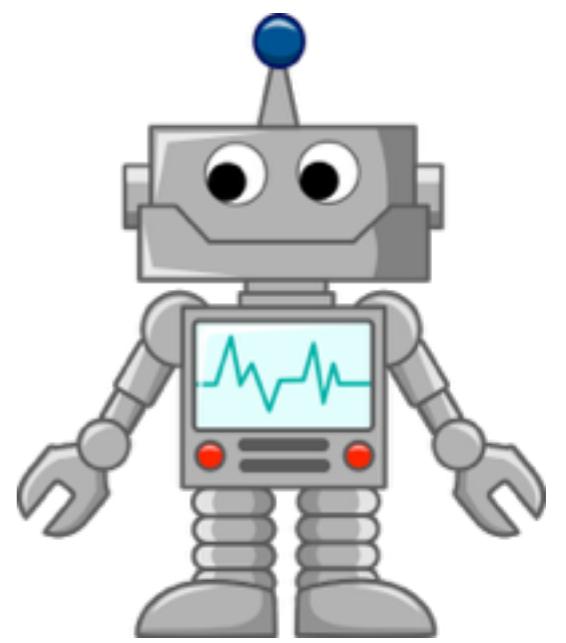
polynomial encoding  
concrete evaluation

```
g0 = a > 0  
g1 = b > 0  
g2 = g0 ∧ g1  
g3 = ¬(g0 ⇔ g1)  
g4 = ¬g0 ∧ ¬g1  
c = ite(g1, b, a)  
assert g2
```

symbolic virtual machine



**solver-aided programming for everyone**



# Recent applications of ROSETTE

## Synthesizing Memory Models from Litmus Tests

James Bornholt and Emina Torlak (under submission)

## Synthesizing Custom Tutoring Rules for Introductory Algebra

Eric Butler, Emina Torlak, and Zoran Popovic (under submission)

## Scalable Verification of BGP Configurations (OOPSLA'16)

Konstantin Weitz, Doug Woos, Emina Torlak, Michael D. Ernst, Arvind Krishnamurthy, and Zachary Tatlock

## Investigating Safety of a Radiotherapy Machine (CAV'16)

Stuart Pernsteiner, Calvin Loncaric, Emina Torlak, Zachary Tatlock, Xi Wang, Michael Ernst, and Jon Jacky

## A Framework for Synthesis and Parameterized Design of Rule Systems Applied to Algebra (ITS'16)

Eric Butler, Emina Torlak, and Zoran Popovic

## Specifying and Checking File System Crash-Consistency Models (ASPLOS'16)

James Bornholt, Antoine Kaufmann, Jialin Li, Arvind Krishnamurthy, Emina Torlak, and Xi Wang.

## Scaling up Superoptimization (ASPLOS'16)

Phitchaya Mangpo Phothilimthana, Aditya Thakur, Rastislav Bodík, and Dinakar Dhurjati.

## Synapse: Optimizing Synthesis with Metasketches (POPL'16)

James Bornholt, Emina Torlak, Dan Grossman, and Luis Ceze.

...



# Recent applications of ROSETTE

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Stuart Pernsteiner, Calvin Loncaric,  
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Enlearn

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# Recent applications of ROSETTE: Neutrons

## Investigating Safety of a Radiotherapy Machine (CAV'16)

Stuart Pernsteiner, Calvin Loncaric,  
Emina Torlak, Zachary Tatlock, Xi Wang,  
Michael Ernst, and Jon Jacky

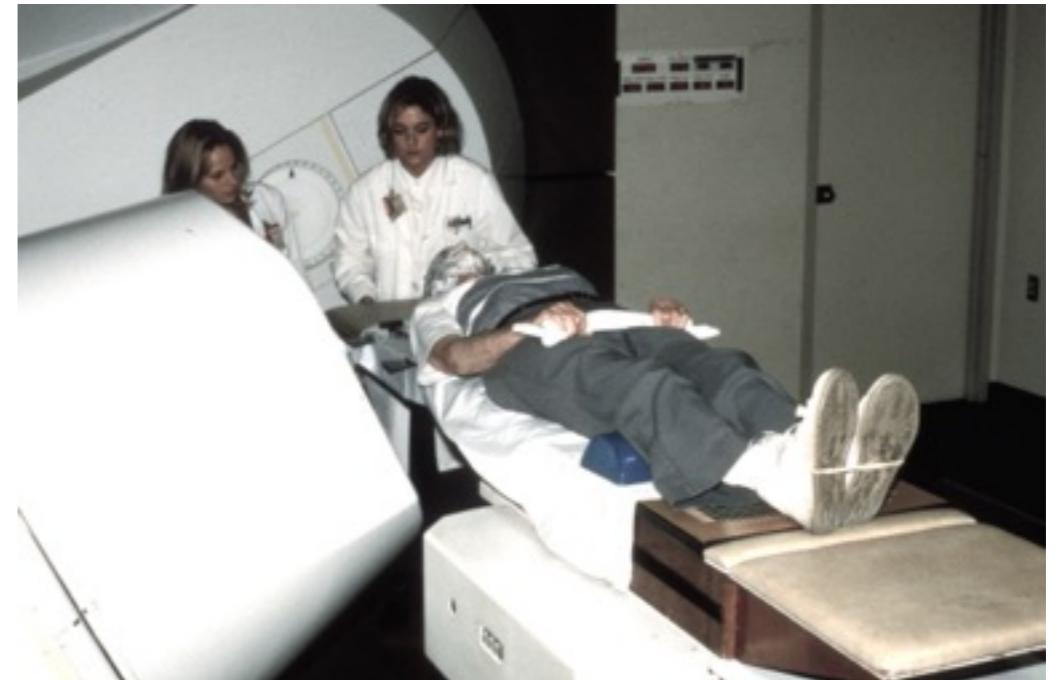


Enlearn

## Scaling up Superoptimization (ASPLOS'16)

Phitchaya Mangpo Phothilimthana,  
Aditya Thakur, Rastislav Bodík, and  
Dinakar Dhurjati.

## Clinical Neutron Therapy System (CNTS) at UW



- Used Rosette to build a verifier for the EPICS DSL.
- Found safety-critical defects in a pre-release version of the therapy control software.
- Used by CNTS staff to verify changes to the control software.

# Recent applications of Rosette: Enlearn

## Investigating Safety of a Radiotherapy Machine (CAV'16)

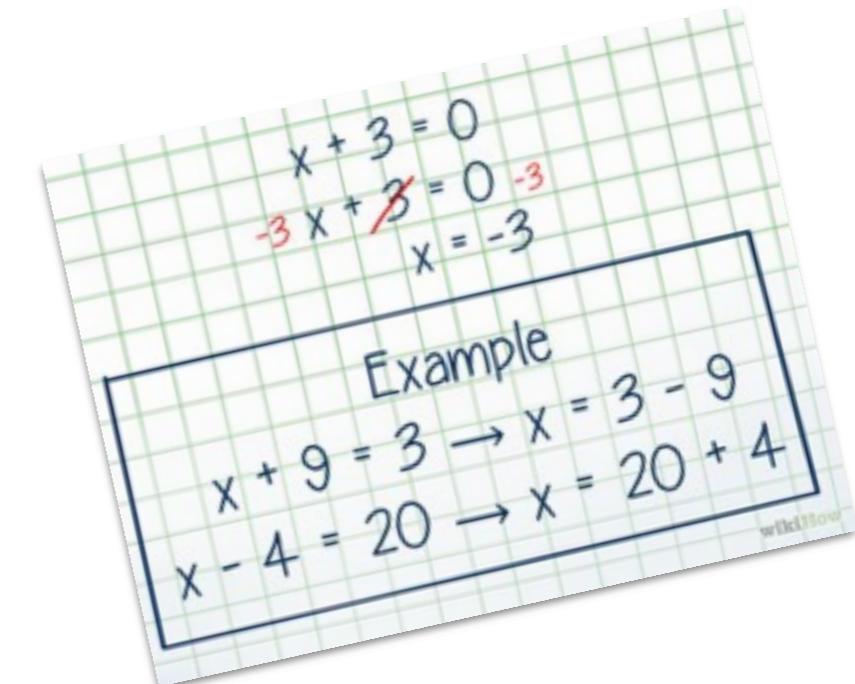
Stuart Pernsteiner, Calvin Loncaric,  
Emina Torlak, Zachary Tatlock, Xi Wang,  
Michael Ernst, and Jon Jacky



Enlearn

## Scaling up Superoptimization (ASPLOS'16)

Phitchaya Mangpo Phothilimthana,  
Aditya Thakur, Rastislav Bodík, and  
Dinakar Dhurjati.



- Education technology company founded by Zoran Popovic.
- Uses Rosette to develop educational math games, released to thousands (and eventually millions) of students.
- Building a Rosette-based DSL to enable educators to develop their own educational tools.

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- A Rosette-based framework for creating efficient superoptimizers for new ISAs.
- Requires only an emulator for the ISA and a few ISA-specific search utility functions!
- Hosts superoptimizers for ARM and GreenArrays (GA) ISA.
- Up to 82% speedup over gcc -O3 for ARM; within 19% of hand optimized code for GA.



**your SDSL**

verify

debug

solve

synthesize

thank you  
**ROSETTE**



**symbolic virtual machine**